

**825™ PRINTER
FIELD SERVICE
MANUAL**



825 FSM. REGION DISTRIBUTION ONLY

This manual contains detailed information on the theory, maintenance, adjustment and parts replacement of the ATARI 825 Printer. The contents of this manual are intended for use by qualified ATARI service personnel who are trained in the maintenance of electronic and electro-mechanical equipment.

The information contained in this manual covers the ATARI 825 (Centronics 737) and the updated replacement models (Centronics 739). The 737 and 739 are identical in theory and in operation.

The major differences between the printed circuit boards are:

1. 739 PCB contains international firmware ROM which contain other character sets.
2. 739 PCB has a DIP Switch to select the character sets.
3. 739 PCB has Plug inputs for front panel Power On LED (no hardware installed).
4. 739 PCB has a Roll Paper Out Switch (no hardware installed).

As long as the 739 PCB contains a 737 Microprocessor IC 8049 (they all do), it performs exactly as a 737 Printer.

SECTION 1
DESCRIPTION

1.1 ATARI 825 80 Column Printer

1.1.1 Printer is a dot matrix impact printer

Can print lines up to 8 inches long in three character sets

Operates under complete control of ATARI 400/800 Personal Computer System or other compatible host device

Computer must be programmed to instruct printer what to do

Recognizes 19 different control codes generated by computer

1.1.2 ATARI 850 Interface Module required for 825 correct operation

Interface module converts the I/O serial data from computer into 7 bit parallel data for Printer operation

1.1.3 Character sets

Print any of following character sets

Monospaced characters at 10 characters per inch (10 CPI)

Monospaced condensed characters at 16.7 CPI

Proportionally spaced characters (average of 14 CPI)

Each set consists of 96 standard ASC II characters

Printer set to print 10 CPI monospaced characters when powered on

Condensed and proportionally spaced character sets must be programmed

Condensed and proportionally spaced characters can be mixed on same print line

10 CPI monospaced characters cannot be mixed with characters of other two sets

When character set selection code has been sent to printer — the printer prints characters in that set until it receives a different character set selection code or powered off

Printer can print up to 80 10 CPI monospaced characters on 8 inch print line

When elongated — can print half as many characters per line

Can print 132 16.7 CPI monospaced characters

Proportionally spaced characters per 8 inch line varies with dot widths of characters -- specs are in dot columns -- maximum number of dot columns per 8 inch line is 1200

1.1.4 Three types of paper used in 825 printer

Fanfold paper -- 9 1/2 inches wide -- 3 ply

Cut sheets to 8 1/2 inches wide

Roll paper 3 1/2 to 8 1/2 inches wide -- 5 inches in diameter -- 1 inch core -- 2 ply

1.1.5 One printed circuit board contains:

All logic

Control

Drive

Power supply circuits

1.1.6 Printing mechanism mounted on carriage assembly that is driven by a DC motor

Unidirectional carriage moves from left to right at a speed of 5 inches per second -- returns to left margin at slightly higher speed

1.1.7 Paper feed (line feed) rollers are driven by stepping motor

Can be controlled locally by PAPER switch

Can be controlled remotely by computer -- generated control codes

1.1.8 Ribbon drive AC motor moves ribbon past printhead

1.1.9 Power supply provides

+5V regulated for logic

+5V external power for computer sensing

+12V regulated for motor drive

+17V unregulated for printer solenoid and motor drive

+24VAC for ribbon motor

1.1.10 Control Switches

POWER ON/OFF switch

When power turned ON -- LED power indicator should light

When set to ON-LINE position

Can receive data and control codes from computer

When set to LOCAL position

Printer is in Local or off-line mode and cannot communicate with computer

PAPER REV/FWD Switch

Used only when printer is in LOCAL (off-line) mode

Spring-loaded and returns to off position when released

Moving and releasing switch caused the paper to advance or reverse one line

Holding switch in position causes paper to advance or reverse continuously

SECTION 2 THEORY OF OPERATION

2.1 BASIC DESCRIPTION (Figure 2-1)

All logic, control, drive and power supply circuits are contained on one printed circuit board. The print mechanism rides on a carriage driven by a DC motor. The printing is performed in the forward direction only, printing up to nine dots per column. The paper feed (line feed) rollers are driven by a stepping motor which is capable of forward and reverse line feed; manual and computer controlled. The ribbon drive (AC motor) moves ribbon past the print head at all times except when the carriage actuates the sensor at the leftmost carriage position. The power supply provides +5V regulated for the logic, +5V EXT for host device sense, +12V regulated for motor drive, +17V unregulated for solenoid and motor drive, and 24 VAC for the ribbon motor.

The host device transmits 7-bit parallel (ASCII) data and a strobe to the printer logic. The printer logic initiates and controls all transmissions from the host by the control signals ON-LINE, DEMAND, ACKNLG and BUSY. When the microprocessor has processed a complete line (or when a print command code is sent), the microprocessor directs the printer to print the characters, column by column. Print head actuation is performed by energizing solenoids that drive circular pins, forming dot matrix characters and underlines. The carriage moves forward at five inches per second, with a somewhat faster carriage return speed.

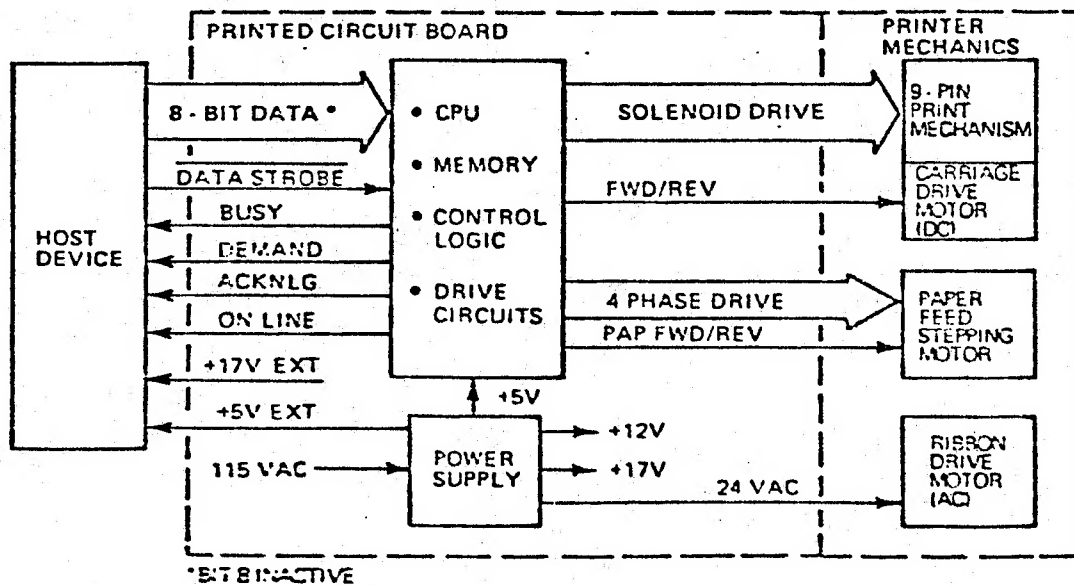


Figure 2-1 BASIC BLOCK DIAGRAM

The printer is capable of printing 80 monospaced characters per line; 10 characters per inch (10 cpi). The monospace matrix is 7x8, in upper and lower-case characters. Monospace descenders (y, g etc.) do not touch the underline (if used), which is provided by activating the 9th pin for all characters to be underlined (including intercharacter and interword spaces if appropriate). Underline may be turned on or off anywhere within a line and as many times as desired, for all character sets. Monospaced characters may be condensed to 16.7 cpi, resulting in 132 character per line.

The proportionally-spaced character set is printed in Nx9 (N = 6 to 18) dot matrix. The proportional descenders (y, g etc.) will run into the underline because the 9th pin is used to form proportional descenders. Proportional text may be right justified through the use of intercharacter and inter-word spaces generated by the host device.

Average character spacing for proportional is approximately 14 cpi; however, numerals 0-9 are monospaced at 12.5 cpi to allow tabulation. Rather than counting characters to determine a full line, dot positions are counted for the proportional set, 1200 dot positions making a full line. If the host device does not transmit a Carriage Return (CR) to end the line at this point, the logic terminates the line with CR after counting and accepting every column of the character that exceeds a count of 1185, regardless of the width of the character (even an elongated W). The remainder of the truncated word (if this is the case) would begin on the next line. For monospaced 10 cpi, CR occurs at 80 characters, and for condensed (16.7 cpi), CR occurs at 132 characters.

All three basic character sets (proportional, monospaced and condensed) may be elongated beginning anywhere in the line after the host transmits ESC, SO. Elongation is accomplished by simply printing each character column twice. This results in half as many characters per inch for all character sets, and a wider, bold-appearing character. ESC, SI terminates the elongated characters.

2.2 DETAILED DESCRIPTION (BLOCK DIAGRAM)

2.2.1 MICROPROCESSOR (ME3; 8049)

The microprocessor controls all communication on the data/address bus, communication to the host and reception of data from the host. The basic clock (6 MHz) provides the time base for all timing pulses, motor steps and sequences used in the printer. ME3 contains 128 bytes of RAM and a storage capability of 255 consecutive Line Feed commands. An Auto LF load resistor is provided so that CR (Carriage Return) codes are followed by a Line Feed without additional command.

Principle input signals to ME3 are as follows: RESET is used to start the power-up and initialize routines. RTP (Ready to Print) is true at the Test 1 input when the carriage is positioned for printing the first character of the line near the left margin. The Busy FF (Q NOT side) is applied to the Test 0 input to indicate storage of new data to be processed. The EA (External Access) input is tied to a Logic 1, directing program memory fetches toward the external memory (ME4 ROM). COLSENSE from the carriage sets the Interrupt FF at the INT input. The rotating magnet on the carriage generates 100 COLSENSE pulses per inch, 800 per line. Internal to ME3, the software converts two COLSENSE pulses to three Column Interrupts when processing proportional and condensed (Figure 2-2). This provides 1200 character columns (positions where pins may or may not be driven depending on the text). The 10 cpi monospaced character set uses 800 columns (80 characters); thus, the special conversion is disabled for 10 cpi monospaced.

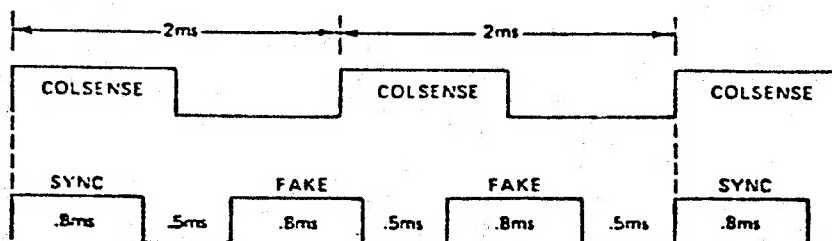
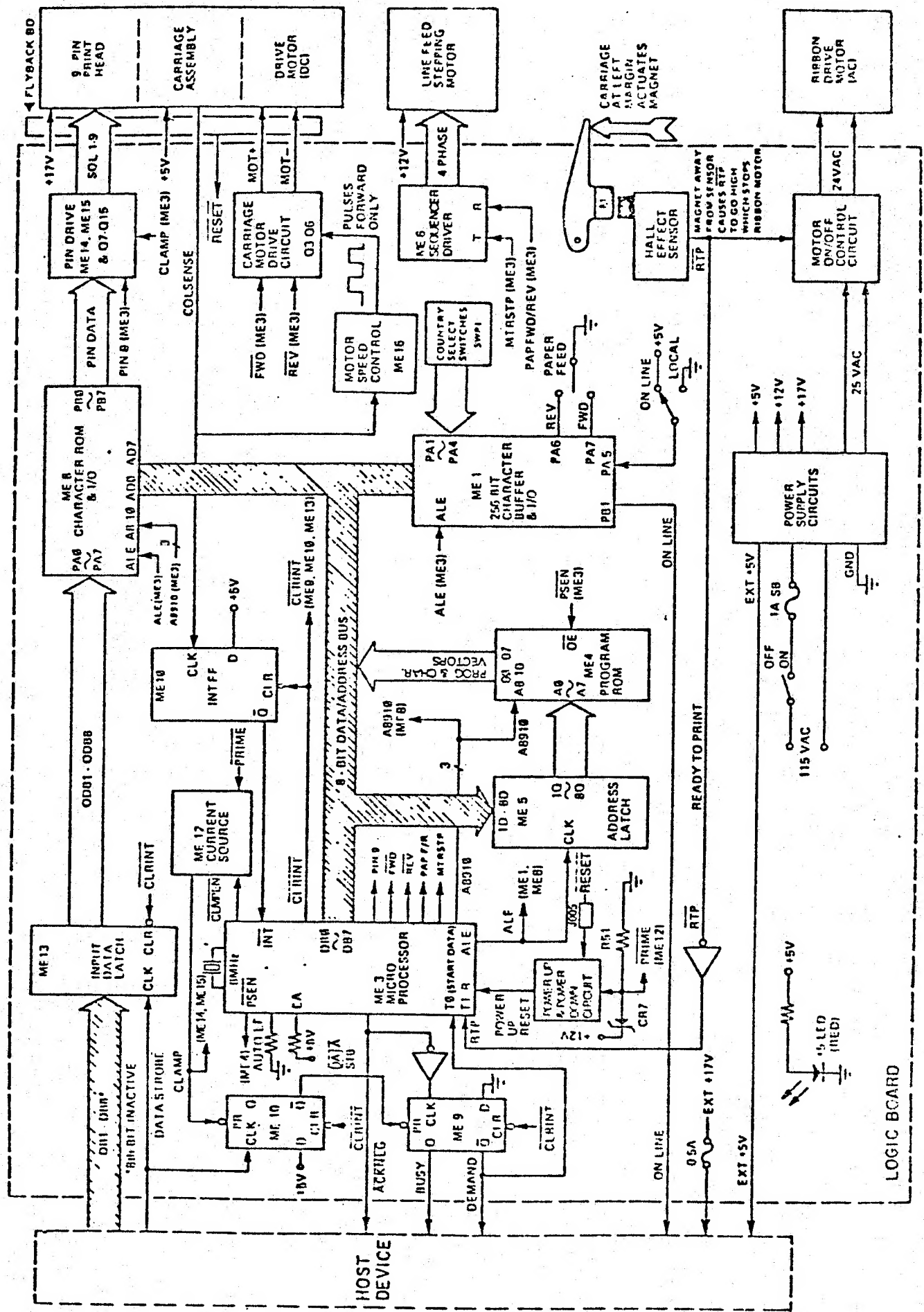


Figure 2-2 SOFTWARE INTERRUPT TIMING (PRINT STROBE)



DETAILED BLOCK DIAGRAM

Principle outputs from ME3 are as follows: ME3 uses DB0-DB7 as a bi-directional bus for transferring data, address and column patterns for pin driving. The program residing in ME4 is transferred to the Microprocessor over this bi-directional bus. Pin 9 from P25 of ME3 carries 9th pin data for underlining all character sets and for descenders of the proportional set. High order bit addressing of ME4 and ME8 is provided by A8910 when 8 bits of address are not adequate. A8910 are the P20, P21 and P22 outputs of ME3. ALE (Address Latch Enable) is sent to ME1 and ME8 to latch the address bits. The bus is then available to accept the data being addressed. ALE also clocks ME5 Address Latch to hold address to ME4 which has no latch capability. PSEN (Program Store Enable) is developed shortly after ALE during a program memory fetch and provides an OE (Output Enable) for ME4, placing the contents of the location addressed onto the bus. CLMPEN (Clamp Enable) pulse triggers ME12 after initialization. This enables ME12 to current source the pin drivers. CLRINT (Clear Interrupts) resets ME13 Latch, the Data Strobe FF and Busy FF. FWD and REV (P11 and P12 of ME3) control the direction of the carriage drive motor. PAP FWD/REV (P26 of ME3) controls the direction of the Line Feed stepping motor. PAP FWD/REV low at ME6 allows normal (forward) line feed. MTRSTP is the Motor Step pulse (P27 of ME3) generated by the internal timer. Eight MTRSTP pulses equal one line feed (one-sixth inch of paper). At the MTRSTP pulse rate of 100 pps, paper moves at 2 inches per second.

Microprocessor outputs not shown in Figure are found on print 63680148 and are as follows: IO/M (P26 of ME3, same line as PAP FWD/REV) selects the I/O or Memory function of ME1 and ME8. IO/M (low) selects RAM in ME1 and ROM in ME8. CE (Chip Enable) for ME8 is output at P23 and CE for ME1 is output at P24. RD (Read the bus) is an output strobe to enable data onto the bus for the microprocessor to read. At ME1, RD allows the microprocessor to read a print character code from RAM or to examine switch settings at Port A. At ME8, RD allows the microprocessor to read a character column from ROM for printing or to accept new data from Port A (ODB1-ODB8). WR (Write the bus) is an output strobe used to write data into an outboard memory. For ME1, WR writes data into RAM or Port B. For ME8, WR writes pin driving pulses onto Port B.

2.2.2 PROGRAM MEMORY (ME4; 2716)

ME4 ROM contains the initialize routine, all program for this system and vectors (starting addresses) for character matrix data contained in ME8. The ROM has 11 address lines, 2048 locations and an 8-bit data output bus. In this circuit, CE (Chip Enable) is actively tied to ground and the data output is enabled by PSEN at the OE input (pin 20). The 8 LSB of address are latched to the A0-A7 inputs by ME5 (clocked by ALE). The three MSB are A8910 (P20, 21, 22 of ME3).

2.2.3 INPUT DATA LATCH (ME13; 74LS273)

The Input Data Latch stores each byte of input data as it is transmitted by the host device. The code format is 7-bit ASCII, positive true logic. The 8th bit is inactive in this system. The host transmits DB1-DB7 for approximately 3 microseconds and a DATA STROBE of about 1 microsecond, transmitted in the middle of the data window. The trailing edge (up-clock) of the strobe clocks the data into ME13. The data is available at the 1Q-8Q outputs until CLRINT clears the latch. DATA STROBE also sets the Data Strobe FF, which presets the Busy FF. The T0 input to the microprocessor goes low, and when the program tests for zero at this input, the new data is processed.

2.2.4 CHARACTER BUFFER AND I/O (ME1; 8156)

ME1 contains a 256 x 8 bit RAM that stores characters not yet printed. All addressing and data transfers are performed on the bi-directional bus AD0-AD7. ALE (Address Latch Enable) is used to latch the Address, CE (Chip Enable) and IO/M inputs. When CE is low, the AD0-AD7 input lines go to a high-impedance state so that this chip does not influence the bus. Latching occurs on the trailing edge (down-clock) of ALE. DATA is written into or read from ME1 depending on the status of WR or RD inputs. IO/M determines whether the RAM or one of the ports is written or read. If IO/M is low, RAM is selected. RESET (high) clears all registers and ports during power up, programming the C/SR (Command/Status Register) bits 0 and 1 so that Port A and Port B are inputs. Later in the initialization routine, Port B is instructed to be an output port. In this system, Port A (PA0-PA7) is used only as an input for examining switches. The ON LINE/LOCAL switch is input at PA5 and is written out at PB1 during a WR cycle by the microprocessor. Data cannot be sent by the host device unless ON LINE (and DEMAND) is active, as the initialization program hangs up in a tight loop until ON LINE is high. In the LOCAL mode, the PAPER REV/FWD switch controls the direction of the Line Feed stepping motor that drives the paper feed roller. Pressing the switch momentarily produces one line feed in that direction. Holding the switch for more than one-half second produces continuous line feed for as long as the switch is held.

2.2.5 CHARACTER ROM AND I/O (ME8; 8755)

ME8 contains an EPROM (Erasable, Programmable Read-Only Memory) of 2048 x 8 capacity in which the character column data reside. Only eight pin positions (vertical) can be stored; thus, for underlining and for proportional descenders, 9th pin data is transmitted by the Microprocessor during the print operation. The starting address for each character is indicated by vectors contained in ME4 Program ROM. The bi-directional bus AD0-AD7 is used to transfer data and addresses. Three additional bits A8-A10 are used as the high order address to the ROM. CE (Chip Enable, low) is actively tied to ground and CE (high) is used to enable ME8. When CE is low, the chip is de-selected and AD0-AD7 go to the high-impedance state. The selection of the ME8 ports (PA, PB) and the direction of data are accomplished by addressing and writing two independent 8-bit registers, DDR A and DDR B (Data Direction Registers A and B). Port A is selected by AD0 and AD1 low; Port B is selected by AD0 high and AD1 low. DDRA is addressed by AD0 low, AD1 high. DDR B is addressed by AD0 high, AD1 high. Although individual lines of each port may be written as input or output, all Port A lines are new data inputs from the host device and all Port B lines are pin drive outputs to the ME14-ME15 high-voltage drivers. All ones written into a DDR make that port an output and all zeroes make that port an input. The DDR's cannot be read. ALE latches the address lines AD0-AD7 and A8-A10, CE, CE and IO/M on the trailing edge (down-clock). RD low reads ROM or an I/O port out to the bus, depending on IO/M. IO/M low reads ROM and IO/M high reads Port A (new data). Because IOR is tied to Logic 1, RD high causes AD0-AD7 to go to a high impedance. IOW low (with CE high) writes Port B with pin drive data, assuming AD0 high was previously latched. With IOW active, the state of IO/M is ignored. RESET high (occurs during power up) programs all port pins to be inputs.

2.2.6 CURRENT SOURCE AND CLAMP (ME12; NE555)

Although the NE555 is normally used as an R-C timer/clock circuit, ME12 acts as a bias current source for the solenoid drivers, Q7-Q15. During normal operation, the bias is on the borderline of transistor conduction so that the pins are driven immediately and reliably when the ME14-ME15 drivers deliver the 800-usec pin drive pulses. Although Q7-Q15 are shown on the print as transistors, they are actually Darlington pairs with internal protection from solenoid transients. ME12 is triggered ON by the microprocessor pulse CLMPEN after initialization and is active as a bias current source until power down. Power down is sensed by zener diode CR7 supplied by +12V. When the zener voltage falls below conduction, the pull-down resistor R51 immediately generates PRIME to shut off ME12 making CLAMP low. This presets Data Strobe FF and Busy FF to inhibit host data transmission, and shuts off bias to the solenoid drive transistors. RESET is also presented to the microprocessor, as ME15-2 provides a rapid discharge sink for C2 (20 uF) through R13 (47 ohms).

2.2.7 CARRIAGE MOTOR DRIVE AND SPEED CONTROL (Q3-A6; ME16)

The print carriage is driven on a rack by a reversible DC motor through a reduction gear train. Motor direction is controlled by FWD and REV from the microprocessor. The basic power source is +17 VDC, and heavy-current polarity switching is provided by power transistors Q3-Q6 connected in complementary-symmetry. These four transistors effectively reverse the motor connection to the +17V supply; Q3 and Q6 conducting when FWD is active, Q4 and Q5 conducting when REV is active.

The Motor Speed Control is a closed-loop feedback circuit for setting and regulating the forward carriage speed. The circuit compensates for variations in temperature and line voltage. Refer to schematic 63680148 for the following description.

The COLSENSE pulse rate (frequency) from the carriage is proportional to motor speed; thus, COLSENSE is used as the tachometer input to Motor Control chip ME16. The differential (+ and -) tachometer inputs are provided by the ME2 inverters. The rate of COLSENSE pulses is 500 Hz (one pulse every 2 msec) at the desired carriage speed of five inches per second. The COLSENSE pulses trigger a pulse generator inside ME16. The resulting constant-amplitude pulses are partially integrated by R55-C29, the pulsating DC applied to the ME16 comparator (-MD). The reference input (+MD) to the comparator is a steady DC level from potentiometer R59 (Speed Adjust). The comparator outputs a modulated DC waveform whose duty cycle is about 50% at the desired motor speed of five inches per second. This pulse-width modulated waveform is applied through the ME7 driver to the power transistor Q3, which intermittently applies +17V to the MOT+ terminal. Ground return from the MOT- terminal is provided by Q6, which is on when REV is inactive.

If the motor tends to slow down, COLSENSE pulses are less frequent, resulting in a greater duty cycle (more ON time than OFF) from the comparator; thus, the motor speeds up to normal. During the carriage return, the Motor Speed Control is not active and the motor operates at about twice the speed as in the forward direction.

2.2.8 CARRIAGE MOTOR STALL TIMEOUT

The carriage motor is protected from burnout by the microprocessor, which monitors COLSENSE pulses. If a print head jam or related failure occurs, the timer in the microprocessor is programmed for an interval of 30 seconds. If no more COLSENSE pulses are developed during this interval, the microprocessor removes power from the carriage drive motor by making FWD and REV both high at transistors Q3-Q6, which disables the drive current.

2.2.9 LINE FEED MOTOR CIRCUIT (ME6; SAA1027)

The Line Feed motor is a reversible, 4-phase stepping motor. 10-ms rep rate pulses are provided by the microprocessor; 8 MTRSTP (Motor Step) pulses produce one-sixth of an inch of paper feed. Forward and reverse stepping are controlled by PAP FWD/REV (P26 of ME3). PAP FWD/REV low at ME6-3 provides forward line feed. P26 is time shared with IO/M control of ME1 and ME8. The field of the stepper motor is supplied with +12V and (pulsed) ground sink is provided by the Q1-Q4 outputs of ME6. The rotor is a multipole permanent magnet whose poles are spaced so that 8 motor steps produce one line feed by turning the drive roller one-sixth inch.

2.2.10 RIBBON MOTOR CONTROL CIRCUIT (Q1, Q2; CR1-CR4)

The ribbon motor runs almost continuously, stopping only when the carriage actuates the Hall effect sensor by moving the magnet away from S1 (UGN 3013) at the left margin. The control works as follows: while the magnet is against the sensor, RTP is low, which turns Q2 on. This allows gate current for Q1 (silicon-controlled rectifier) which provides a conductive bridge for diodes CR1-CR4. Two diodes conduct with each alternation from T1; either CR1+CR4 or CR3+CR2. Thus, 24 VAC is applied to the AC motor (the diodes do not rectify, they are switches). When the magnet is moved away from the sensor, RTP goes high and Q2 shuts off, removing gate current from Q1, opening the bridge and stopping the motor. If the carriage moves forward again to prepare for another line of printing, the magnet actuates the sensor, RTP goes low and the microprocessor prepares the print routine.

2.2.11 POWER SUPPLY CIRCUIT

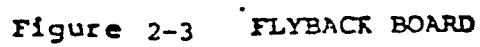
Transformer T1 steps the line voltage down to 25 VAC center-tapped for full-wave rectification by CR5 and CR6. Large filter capacitor C3 (10K uF) provides pre-filtering for the regulator circuits and is the dynamic current source for the solenoids. C3 charges up to the peak of the rectified waveform and is +17 VDC before heavy loading. +17 VDC is applied to regulators VR1 (EXT +5V), VR2 (+12V) and VR3 (+5V Logic). The regulator circuits have post-filtering provided by C5, C6 and C7. Indicator LED (DS1) glows red to indicate that the +5V logic supply is functioning.

2.2.12 FLYBACK PROTECTION CIRCUIT

The flyback circuit is contained on a small PCB that plugs into J004 on the Logic PCB. A flexible ribbon cable completes the circuit to the print head. The circuit works as follows. (Refer to Figure 2-3 for schematic):

The silicon diodes and zener diodes protect the solenoid drive transistors from switching transients. Additionally, a 2.5A fuse in series with each solenoid provides overload protection. Capacitor C1 filters the commutator noise from the DC motor and C2 filters the +5V line to the Hall effect sensor that produces column sense (COLSENSE) pulses.

The electronic circuitry generates a reset to the microprocessor whenever there is a print head jam, paper jam etc. This reset removes power from the carriage drive motor and solenoids. Operational amplifiers monitor MOT + and MOT - to determine when the motor is powered. Column sense pulses are monitored by charging a capacitor (C5) at the input to another operational amplifier. Solenoid current is monitored at Q1 as an additional condition for generating the reset. Failure of COLSENSE pulses for about 250 milliseconds allows C5 to discharge. This failure, along with power to motor or current, through any solenoid applies a reset (low) through J005 to ME15-pin 2 which discharges C2 on the Logic PCB. This resets the microprocessor and associated logic circuitry.



2.3 POWER UP/DOWN ROUTINES (MAIN BLOCK DIAGRAM)

2.3.1 POWER UP/INITIALIZE ROUTINE

When the power switch is turned on, the +5V logic supply gets up to Logic 1 (approximately +3V) in a few milliseconds. A R-C delay provided by C2 (20 uF) and R12 (12K ohms) holds a low on pin 3 of inverter ME11 for 100 milliseconds. Inverter ME7-8 holds the RESET input to microprocessor ME3 low. ME11-4 provides a high level to the RESET inputs of ME1 and ME8. The CLAMP from ME12 is low, disabling the solenoid drivers and presetting Data Strobe FF and Busy FF to inhibit host data. After the power-up reset circuit C2-R12 times out, the microprocessor performs the initialization routine which includes the following: disable column interrupt with CLRINT, which also clears the Input Data Latch ME13 and Data Strobe FF, releasing the preset on the Busy FF; addressing the DDR's and CSR's of ME1 and ME8 to set up the ports; prevent pin firing; select the primary character set (10 cpi monospaced); clear 8049 memory and move the pointer to the top of RAM; clear the data buffer in ME1; enable the solenoid drive current source ME12 with CLMPEN; check for print head at left margin (RTP) and home it if required; check for ON LINE at ME1 (loop here if LOCAL) and prepare for host data.

2.3.2 POWER DOWN ROUTINE

Whenever the AC line voltage takes an extended dip, or if power is lost to the printer for any reason, the printer circuitry prevents pin driving and data transmission from the host device as follows: The +12V applied to zener diode CR7 falls below the zener voltage and resistor R51 (150 ohms) generates PRIME (low) before the +5V logic supply falls below logic 1. PRIME makes CLAMP go low, removing solenoid driver bias (disabling pin driving). CLAMP also presets Data Strobe FF which sets Busy FF, inhibiting data from the host device. The microprocessor (ME3), ME1 and ME8 are also reset. All print codes in ME1 data buffer are lost. When the host device polls the printer status, ON LINE is low and EXT +5V is low, indicating power down at the printer.

2.4 INPUT DATA CYCLE

Data is sent by the host device when ON LINE is active and ACKNLG has set DEMAND to invite data. The character set has been selected by power up or by the host transmitting ESC, DC1; ESC, DC3 or ESC, DC4. The host sends a 7-bit ASCII code and a strobe. The code is latched at ME13 and made available at ME8, Port A. The strobe alerts the 8049 microprocessor that a new input code is available, so the 8049 calls the code in, using the 8-bit bidirectional bus. BUSY is set to inhibit transmissions from the host until this code is completely processed. The 8049 strips the 8th bit, ignores NULL codes (0000000) and treats deletes (1111111) as NULL codes. No operation takes place for NULL's and the next data is called in.

The 7-bit ASCII code is decoded in the 8049 to determine if it is a control code, in this sequence: BS, LF, CR, ESC, SO and SI. If not a control code, the dot column width of this character or space is determined, considering whether Elongation is active and whether the character is 10 cpi, 16.7 cpi or proportional. The character count is also kept (except for proportional) so that the Buffer Full condition can be determined to end the line if the host neglects sending CR. Thus, the dot column width is added to the column count and the 7-bit ASCII code is sent to the 8156 RAM (ME1) where it is stored (buffered). Note that some control codes (SO, SI) are immediately buffered and the next code is called in. Buffered codes comprise the line to be printed, when appropriate.

If an ESC control code is decoded, an Escape sequence is recognized and the next code is called to be examined. For example, if the code following ESC is SO, the Set Elongated Characters sequence is decoded and this condition is set in the 8049. Characters and spaces following this sequence will be elongated, the column count being doubled for each character and space. If the code following ESC is decoded as a character whose dot column width is less than 7, a Justification Space sequence is recognized (ESC, 1 thru ESC, 6). The column count is increased by the designated space and the code is buffered as a character.

When CR is decoded, or when Buffer Full occurs, the program calls the Print Routine. Note that all other control codes that are terminators also call the Print Routine.

2.5 PRINT CYCLE

The Print Cycle is entered when a CR code is received or a Buffer Full condition exists. Any terminator control code will also result in the program switching from the Input Data Cycle to the Print Cycle. Assuming that the carriage is at the left margin and has stepped forward to generate RTP (Ready to Print), the print head is in position to drive pins to form characters. A line of characters, spaces and control codes is held in the Print Buffer (ME1; 8156) in the form of 7-bit ASCII codes.

The first 7-bit ASCII code is transferred from the Print Buffer to the 8049 over the 8-bit bidirectional bus. The character is decoded to determine whether it is a control code (such as an ESC, DC3 sequence to select 10 cpi) or a printable character. If a printable character such as upper case A, a separate table must be consulted depending on whether the character set is monospaced or proportional. The 8049 adds an offset to the ASCII code and addresses the 2716 ROM (ME4) to obtain the vector for this character. The vector is the starting address of the pin data for the character. The pin data resides in MEB ROM in the form of vertical columns, each column in an adjacent location. For proportional characters, both the starting and ending addresses must be determined because the width format is not constant (N=6 to 18 dots wide).

To print characters, the 8049 addresses the MEB ROM to transfer the pin data over the bidirectional bus. After possible minor reconstruction, each sequential column of data is transmitted back over the bus to drive the pins to form the character as the print head moves to the right. Synchronization for pin drive is provided by COLSENSE pulses from the carriage. For spaces, no pins are driven. If proportional descenders require 9th pin drive, the vertical position of the column within MEB ROM is shifted up by one dot so that the 9th pin data is sent to the 8049 on the 8th data line. Characters with descenders have no ascenders, so no data is lost in the vertical shift. In the 8049, the column is shifted back down and the 9th pin is transmitted separately but synchronized with the 8-bit drive.

Underline data is not contained in MEB ROM as a character; continuous 9th pin drive is issued directly from the 8049 while underline is active. Underline and 9th pin descenders merge as a single 9th pin drive. Only the vertical rule (with underline active) drives 9 pins simultaneously.

When proportional and 16.7 cpi monospaced characters are being printed, the additional print strobes (described previously) are generated from COLSENSE pulses (3 print strobes from 2 COLSENSE pulses) so that more characters per inch are printed although the carriage moves forward at a constant 5 inches per second.

Whenever a printed line is interrupted by an ESC, FS (Half Line Feed) or similar control code, the carriage always returns to the left margin sensor and moves forward, counting COLSENSE pulses to reach the appropriate position to print the next "line". This is done to obtain the greatest positional accuracy because overshoot caused by carriage momentum is washed out in this manner.

SECTION 3

MAINTENANCE AND TROUBLESHOOTING

3.1 GENERAL

This section contains information on maintaining and troubleshooting the printer. Routine preventive maintenance is contained in the Operators Manual. Troubleshooting procedures contained in this section aid in isolating malfunctions to defective assemblies or required adjustments.

3.2 SERVICE CLEANING AND LUBRICATION

This cleaning and lubrication procedure should be performed by qualified service personnel whenever the printer requires servicing or repairs which require the cover assemblies to be removed. The procedures should be performed prior to detailed service and repair procedures as some malfunctions can be caused by dust, ribbon chaff, paper chaff, residue build-up, or inadequate lubrication.

1. Remove the printer cover assemblies per instructions in Operators Manual.
2. Visually inspect the interior of printer for loose wires, connectors, and hardware, chaffing of cables, and badly worn or damaged parts.
3. Remove the Mechanism Assembly per paragraph 5.3 and set it to one side.
4. Using a soft bristle brush sweep away the paper and dust residue from the printer base and printed circuit board.
5. Clean the print head and carriage assembly using a soft bristle brush to remove dust and residue.
6. Gently remove all dried ink from the front of the print head using a soft, clean, lint-free cloth.
7. Clean contaminated parts of the rest of the mechanism assembly using a freon-moistened lint-free cloth. Pay particular attention to the carriage shafts and the paper roller end-plate bushings.
8. Clean the carriage shafts by applying several drops of lubricating oil to a soft, clean lint-free cloth, then moving the print head to the left side of printer and lightly rubbing the shafts to remove residue build-up. Move print head to the right side of the printer and clean the left side of the shafts.
9. Clean the paper drive roller and the end plate bushings using a soft, clean, lint-free cloth, then apply one drop of light lubricating oil to both paper roller end plate bushings. Cycle the paper roller using the thumbwheels to allow the oil to seep into the bushings.

10. Reassemble the printer and continue with service procedures.
After completion of service, clean all external surfaces using a mild detergent and a soft, clean, lint-free cloth.

3.3 TROUBLESHOOTING

Table 3-1 list some malfunctions which may occur, the probable causes, and the remedies which may be performed by qualified service personnel. The procedures referenced in the detailed troubleshooting chart should be performed by qualified service personnel who have been trained to maintain and repair complex electronic and electromechanical equipment.

Table 3-1. Detailed Troubleshooting Chart

<u>TROUBLE</u>	<u>PROBABLE CAUSE</u>	<u>REMEDY</u>
Printer on, but inoperative.	Paper Out Switch needs adjustment.	Adjust per
	Fuse F1 blown.	Check if "Power On" LED is lit by looking down through paper exit slot. If not lit, replace fuse F1 with a good fuse of the same rating.
Head drags (wide smudge on paper).	Head gap improperly adjusted.	Adjust printer head gap.
	Defective head-carriage assembly.	Replace defective head-carriage assembly.
Light smudge under print line.	Head gap improperly adjusted.	Adjust printer head gap.
	Defective head-carriage assembly.	Replace head-carriage assembly.
Tears or creases in paper (jams).	Head gap improperly adjusted.	Adjust printer head gap.
	Defective head-carriage assembly.	Replace head-carriage assembly.
Print too light.	Head gap improperly adjusted.	Adjust printer head gap.
	Worn out or defective ribbon.	Replace ribbon.
	Defective head-carriage assembly.	Replace head-carriage assembly.
	Print head release lever open or partially open.	Rotate print head release lever clockwise to normal printing position.

Table 3-1. Detailed Troubleshooting Chart (cont'd)

<u>TROUBLE</u>	<u>PROBABLE CAUSE</u>	<u>REMEDY</u>
Left hand margin wanders.	Defective printed circuit board.	Replace printed circuit board.
	Defective mechanism assembly.	Replace mechanism assembly.
Prints too slowly.	Printer requires general cleaning and lubrication.	Clean and lubricate printer.
	Rotor/Hall effect device improperly adjusted.	Adjust rotor/Hall effect device.
	Print density (speed) improperly adjusted.	Adjust print density (speed).
	Defective printed circuit board.	Replace printed circuit board.
	Defective head-carriage assembly.	Replace head-carriage assembly.
	Defective mechanism assy.	Replace mechanism assembly.
Prints erratically (missing dots).	Defective 9-wire head ribbon cable assembly.	Replace 9-wire head ribbon cable assembly.
	Defective 8049 micro-processor.	Replace 8049 micro-processor.
	Defective printed circuit board.	Replace printed circuit board.
	Defective FF head.	Replace FF head, flex circuit.
Same dot always missing.	Defective ribbon cable.	Replace 9-wire head ribbon cable assembly.
	Defective flyback board.	Replace flyback board.

Table 3-1. Detailed Troubleshooting Chart (cont'd)

<u>TROUBLE</u>	<u>PROBABLE CAUSE</u>	<u>REMEDY</u>
Pin No. 9 prints too lightly.	Defective head-carriage assembly.	Replace head-carriage assembly.
	Defective platen assembly.	Replace platen assembly.
Line feed operates improperly.	Printer requires general cleaning and lubrication.	Clean and lubricate printer.
	Line feed stepper belt/pulley improperly adjusted.	Adjust belt tension or tighten pulley.
	Defective line feed stepper motor assembly.	Replace line feed stepper motor.
	Defective mechanism assy.	Replace mechanism assembly.
	Defective printed circuit board.	Replace printed circuit board.
Carriage emits thumping noise while printing.	Printer requires general cleaning and lubrication.	Clean and lubricate printer.
	Defective head-carriage assembly.	Replace head-carriage assembly.
	Defective carriage molded rack assembly.	Replace carriage molded rack assembly.
	Defective mechanism assy.	Replace mechanism assembly.
Excessive print density at beginning of print line (overprint).	Defective printed circuit board.	Replace printed circuit board.
No ribbon movement.	Ribbon drive motor connector J002 unplugged.	Check for proper connection.
	Defective ribbon drive motor.	Replace ribbon drive motor.
	Defective printed circuit board.	Replace printed circuit board.

Table 3-1. Detailed Troubleshooting Chart (con'd)

<u>TROUBLE</u>	<u>PROBABLE CAUSE</u>	<u>REMEDY</u>
Erratic Carriage/ Tractor Movement	Defective Logic PCB	Replace Logic PCB
Tractors will not move	Defective Logic PCB	Replace Logic PCB
	Defective Head Carriage Assy	Replace Head Carriage Assy
Tractors move backward after 10-15 min print or no print	Defective Head Assy	Adjust/repair/replace Head Assy
	Defective Flex Cable	Repair/replace Flex Cable
Tractors move forward continuously	Defective Logic PCB	Repair/replace Logic PCB
Ribbon does not feed properly.	Ribbon twisted or im- properly loaded.	Check ribbon threading and correct as required.
	Ribbon drive rollers not engaged properly.	Open and release driven roller. Rotate driven roller clockwise to ensure proper movement of ribbon.
Power applied/data sent-printer does not print.	Cable between input de- vice and printer not connected.	Check that connectors at both ends of data input cable are properly connected to mating connectors.
	ON-LINE/LOCAL switch in LOCAL position.	Place switch in ON-LINE position.
Print contrast varies across print line.	Head gap improperly adjusted.	Adjust printer head gap.
	Defective head-carriage assembly.	Replace head-carriage assembly.
	Defective mechanism	Replace mechanism assem- bly.
Roll Paper/Cut-sheet paper does not advance properly.	Pinch roller release lever in open position.	Pull pinch roller release lever forward to closed position.

SECTION 4

ADJUSTMENTS

4.1 INTRODUCTION

Certain malfunctions and marginal operation may require that the printer be adjusted. Adjustment should also be performed whenever an affected part is replaced in the printer. The following adjustment procedures are contained in this section.

- Printer Head Gap Adjustment
- Rotor/Ball Effect Sensor Clearance Adjustment
- Print Speed Adjustment

All of the procedures contained in this section are performed with the printer cover assemblies removed. Refer to the Operators Manual for the procedure for removing the printer cover assemblies, and to Section 5 for other removal procedures as required..

Only the Print Speed adjustment is performed with power applied to the printer. As this procedure is performed with the cover assemblies removed, care must be taken.

WARNING

Care must be taken when adjusting the printer with the covers removed and power applied to the printer as injury can result from contact with moving parts of the printer or areas within the printer where hazardous voltages are present.

Before performing any adjustment procedure contained in this section, read the procedure carefully to ensure that it is understood, that the appropriate tools and accessories are available, and that care is exercised when performing the adjustment. Return all ground wires to their proper connectors when reassembling covers.

4.2 TOOLS REQUIRED

The procedures contained in this section require the following tools:

Tools

- | | |
|---------------------------------|------------------------------|
| • 1/4 inch flat tip screwdriver | • No. 1 Phillips screwdriver |
| • 1/4 inch nut driver | • .004 feeler gage |
| • 5/8 inch nut driver | • .005 feeler gage |
| • Ohmmeter | • .014 feeler gage |
| • 3/8 inch ignition wrench | • 2 mm Allen wrench |
| | • .050 Allen wrench |

4.3 PRINTER HEAD GAP ADJUSTMENT

1. Remove the printer cover assemblies and remove the mechanism assembly from the base.
2. Carefully move the print head to the center of the printer mechanism. Handle the carriage by the sturdy casting area above the carriage rack.
3. Refer to Figure 4-1. Place the print head release lever in position 3 and insert a .014 inch feeler gage between the nose of the print head and the platen.
4. Loosen the set-screw located on the left side of the head carriage assembly using a 2 mm Allen wrench.
5. Adjust the cam-bushing adjusting nut (using a 3/8" open-end wrench) located under the left side of the head-carriage assembly until the .014 inch feeler gage slips easily between the print head nose and the platen.

NOTE

Ensure that the print head release lever is maintained in position 3 during this adjustment.

6. Tighten the set-screw loosened in step 4.

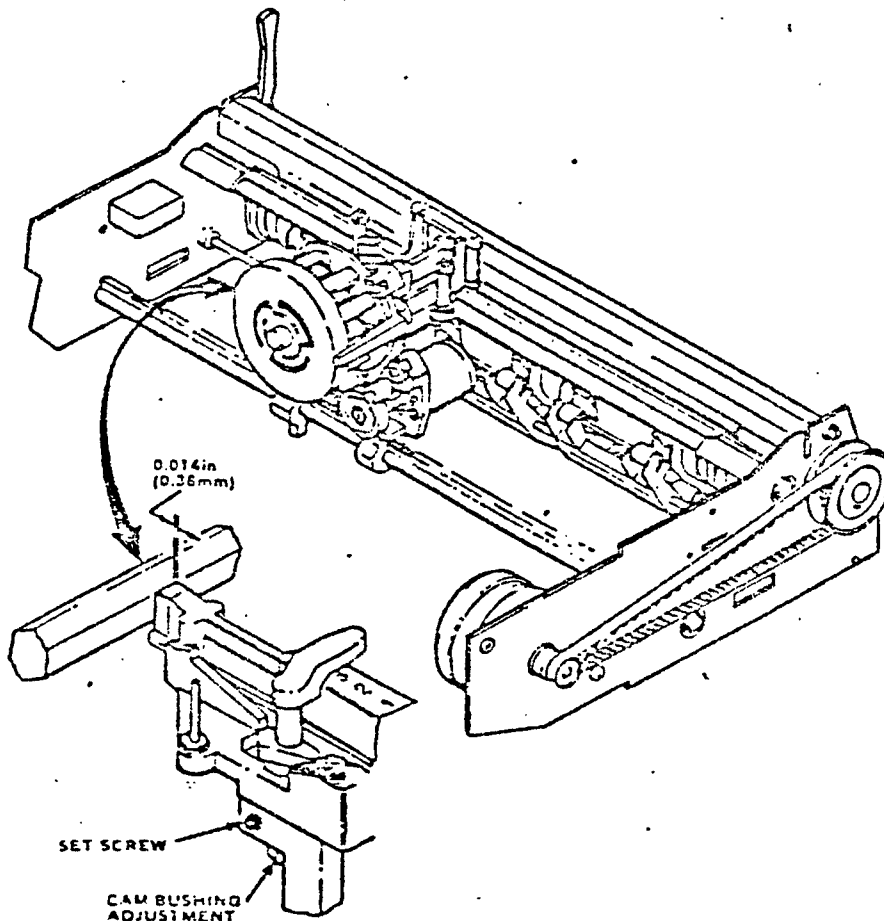


Figure 4-1. HEAD GAP ADJUSTMENT

4.4 ROTOR/HALL EFFECT SENSOR CLEARANCE ADJUSTMENT

1. Remove printer cover assemblies and remove mechanism assembly from printer base.
2. Refer to Figure 4-2. Loosen the Hall effect sensor mounting screw on the head carriage assembly using a No. 1 Phillips screwdriver.
3. Insert a .005 feeler gage between the sensor and the magnetic rotor.
4. Move the sensor against the feeler gage until the gage can be moved with slight friction, then tighten the mounting screw.
5. Insert a .004 feeler gage between the sensor and the magnetic rotor. The gage should fit loosely.
6. Carefully move the head carriage assembly and observe the rotor. The rotor should rotate without touching the sensor.

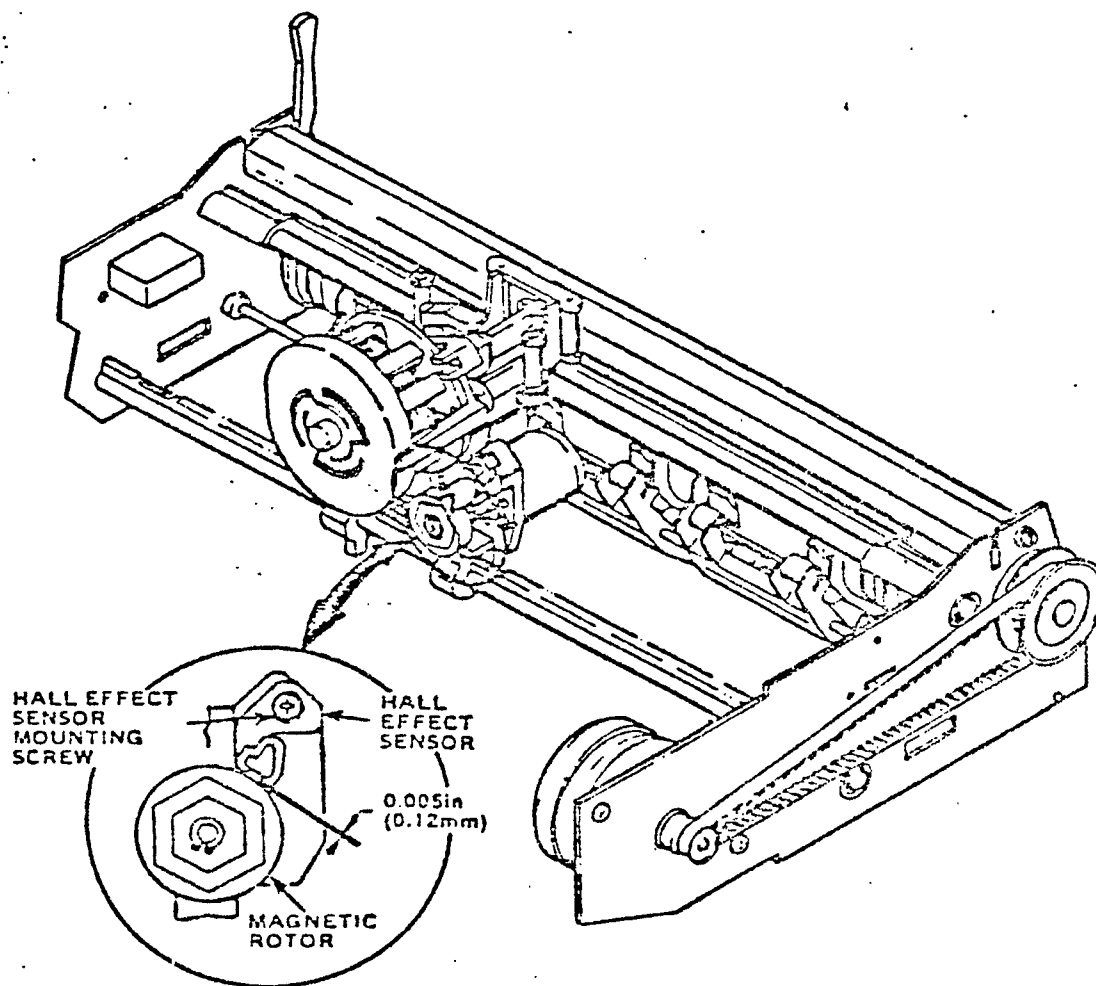


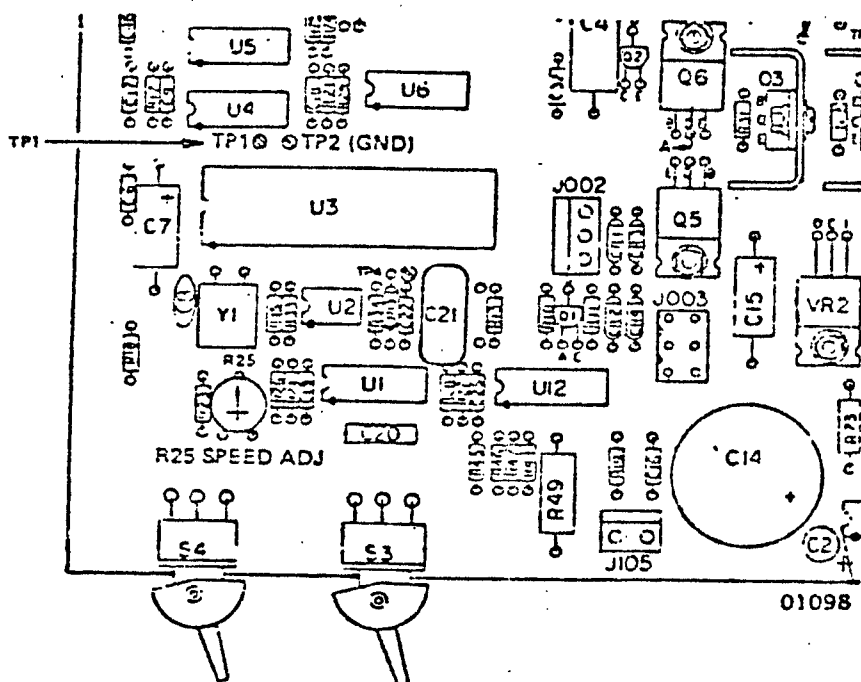
Figure 4-2. HALL EFFECT SENSOR ADJUSTMENT

4.5 PRINT SPEED ADJUSTMENT

1. Remove cover assemblies.
2. Set up printer for self-test per Operators Manual (paragraph 5.5).
3. Place POWER ON/OFF switch in ON position and observe that the power on LED illuminates.
4. Place ON-LINE LOCAL switch in ONLINE position. The print head should move to the left margin, then to the right margin and back. Each time the print head reaches the right margin and reverses direction a line feed is executed. This action continues until the printer is turned off or the ONLINE/LOCAL switch is placed in the LOCAL position.
5. Determine the number of times the print head moves from left to right in one minute (lines per minute) by observing print head movement and using a watch or clock. The proper rate is from 37 to 39 lines per minute.
6. Adjust potentiometer R25 until the proper rate (37 to 39 lines per minute) is achieved.

NOTE

If a scope is available, monitor Column Interrupts at TP1 or U3, pin 6 (see Figure 4-3). Adjust potentiometer R25 for a time interval of 1.0 to 1.1 milliseconds between pulses (with carriage moving forward).



SECTION 5

REMOVAL AND REPLACEMENT

5.1 SCOPE OF THIS SECTION

This section contains removal and replacement procedures which should be performed by trained service personnel. Some procedures require special tools and adjustments. Replacement of line fuse F1 and the automatic line feed resistor are contained in Section 5.

WARNING

Place power switch in the OFF position and remove plug from the wall outlet before performing any procedures in this section.

5.2 MECHANISM ASSEMBLY (Reference Figure 5-1)

When replacing the mechanism assembly, be certain to observe the CAUTIONS to avoid damage to the printer.

- a. Place power switch in the OFF position and remove plug from the wall outlet. Remove the top cover and body cover.

CAUTION

Before removing the connector from J003 in step b., move the print carriage to the extreme right to allow room to grasp the connector and to avoid damage to the carriage assembly. Also, support the circuit board at the base of J003 by pressing down with the fingers of the other hand while pulling up on the body of the connector. DO NOT pull on the wires.

- b. Remove the stepping motor power connector from J003. See CAUTION above.
- c. Carefully unplug the ribbon connector from the Flyback Board Connector at the middle of the printed circuit board. Unplug J005 single wire connector.
- d. Release the mechanism assembly from the printer base by pulling out slightly on the retaining tabs at both sides. Lift the mechanism assembly up and out.
- e. Carefully unplug the ribbon cable from the connector on the carriage and save the cable for the replacement mechanism.

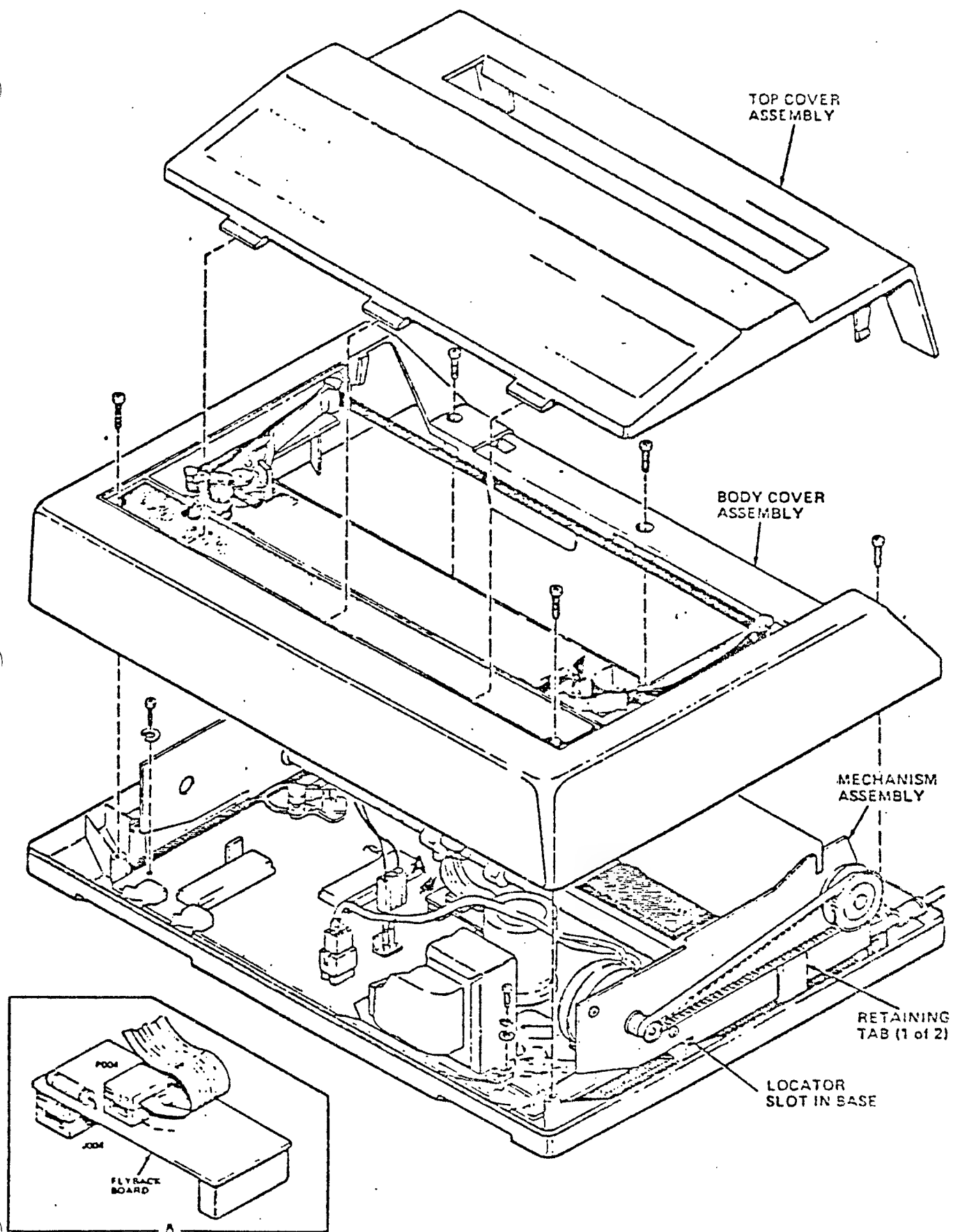


Figure 5-1 MECHANISM ASSY. REPLACEMENT

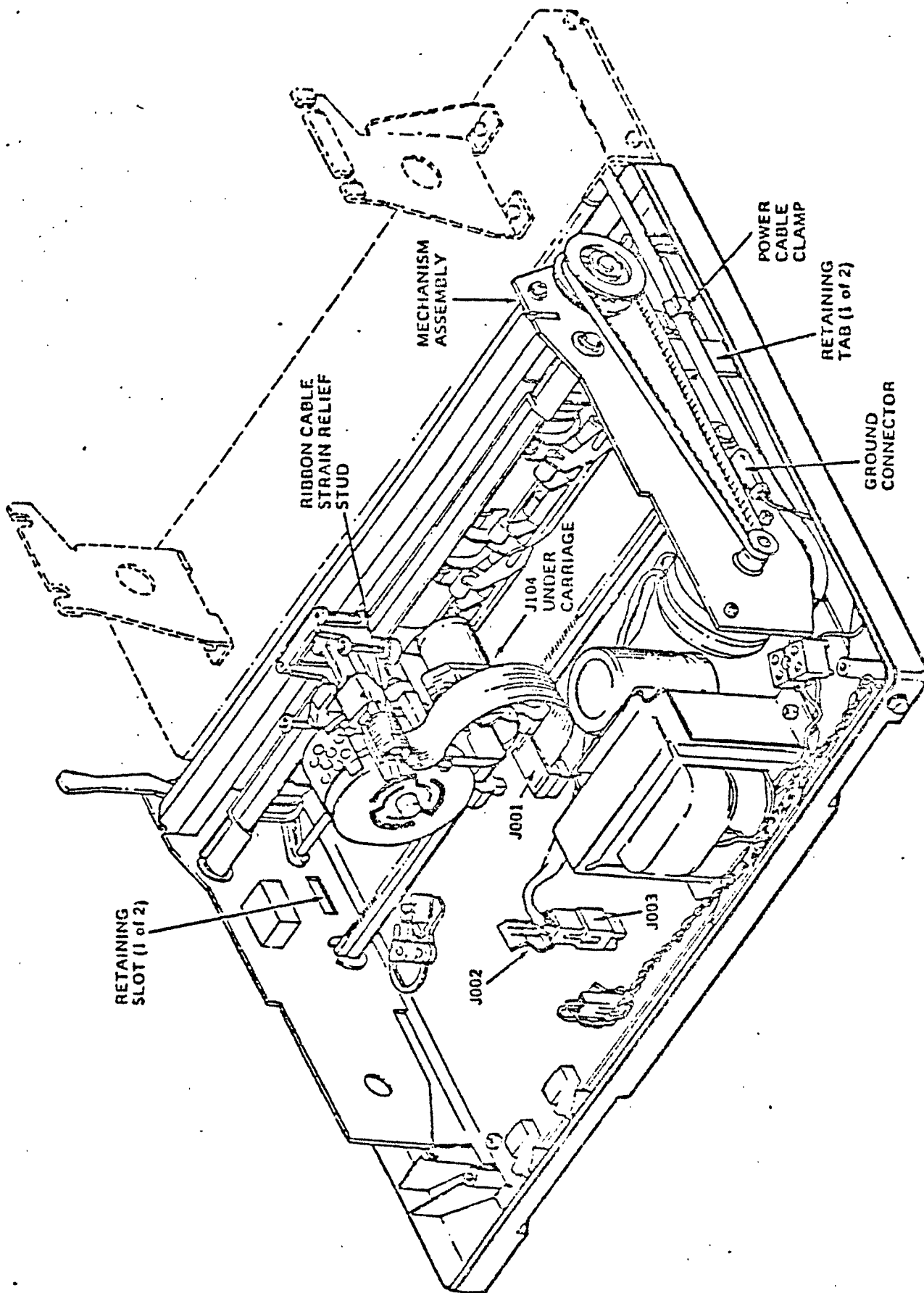


Figure 5-1a MECHANISM ASSEMBLY REPLACEMENT

CAUTION

Before installing replacement mechanism assembly, check/adjust the following clearances: print head to platen and Hall effect sensor to magnetic rotor.

IMPORTANT -

Move the carriage to the right side before installing the replacement mechanism assembly; otherwise, the RTP actuator arm may erroneously be positioned to the left side of the RTP assembly, which would result in damage to the carriage rack.

- f. Install the replacement mechanism assembly by carefully indexing the side plates into the locator slots in the base, then lock the retaining tabs into the slots on the side plates.
- g. Install the ribbon cable, plugging it carefully into the sockets on the carriage and the flyback board (see inset, Figure). The ribbon cable must loop behind the strain-relief bar on the carriage assembly. Plug in single wire connector J005.
- h. Plug the stepper motor cable connector into J003.
- i. Reassemble the top cover and body cover.

5.3 PCB LOGIC BOARD (Reference Figure 5-2).

When replacing the PCB logic board, removal of the assemblies is required as follows:

- a. Place power switch in the OFF position and remove plug from the wall outlet. Remove the cover assemblies
- b. Remove the mechanism assembly. Be certain to read the CAUTIONS for removal/replacement of the mechanism assembly.
- c. Remove one Phillips-head screw at the left front corner of the PCB and two Phillips-head screws at transformer T1. Save the transformer heat shield and plastic high voltage (fuse) shield for replacement.

CAUTION

When removing the PCB from the base, or when inserting the replacement PCB, take care not to damage the switches, which extend out of the front panel.

- d. Disengage the rear edge of the PCB by sliding it toward the front. Lift up at the rear edge and carefully pull the PCB out of the base, disengaging the line cord strain relief from the base.
- e. Install the replacement PCB, making certain that the shields from step c. are properly installed, and that the line cord strain relief is indexed properly at the rear of the base. Refer to steps d., c., b., and a. as required for reassembly.

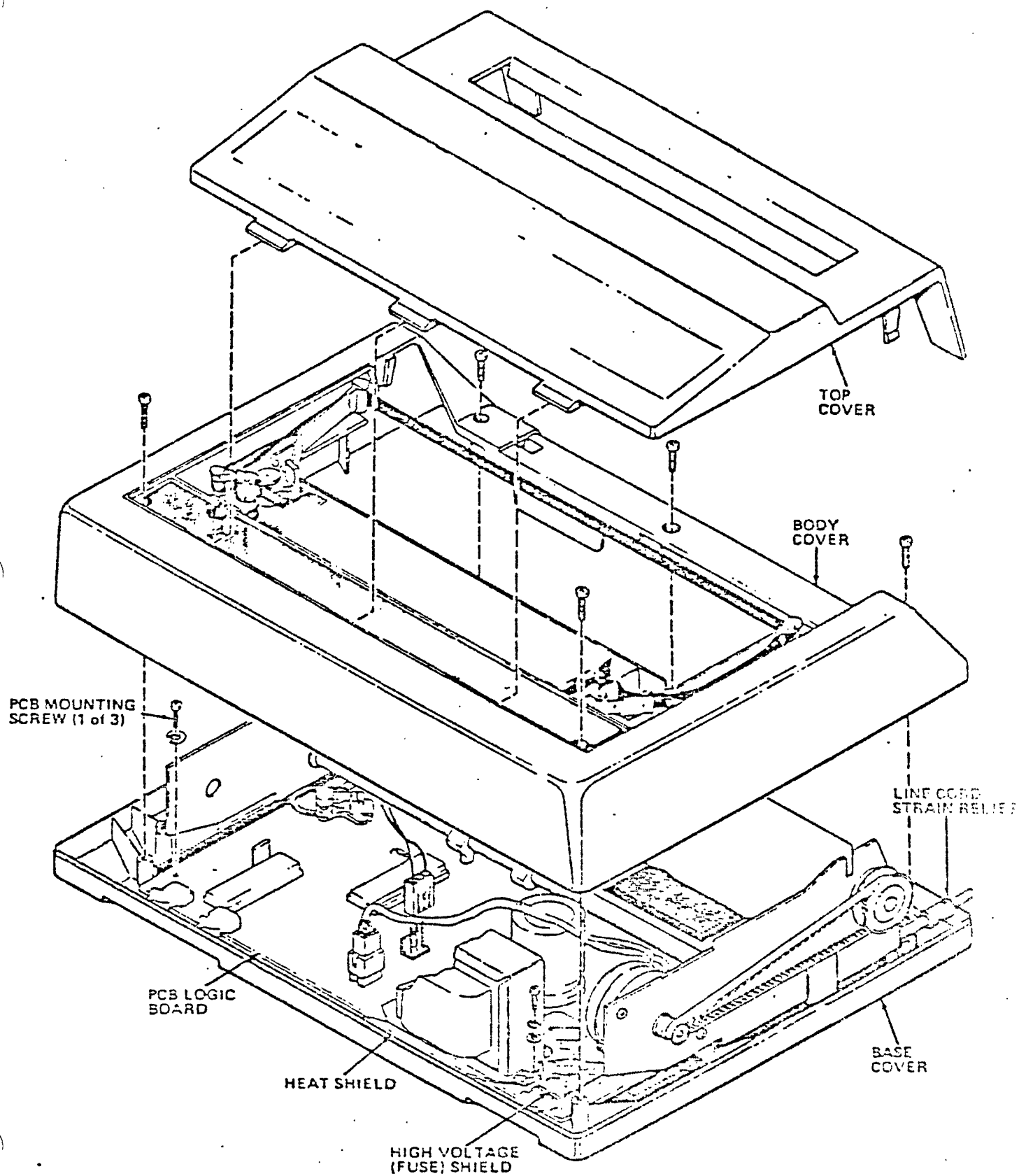


Figure 5-2. PCB LOGIC BOARD REPLACEMENT

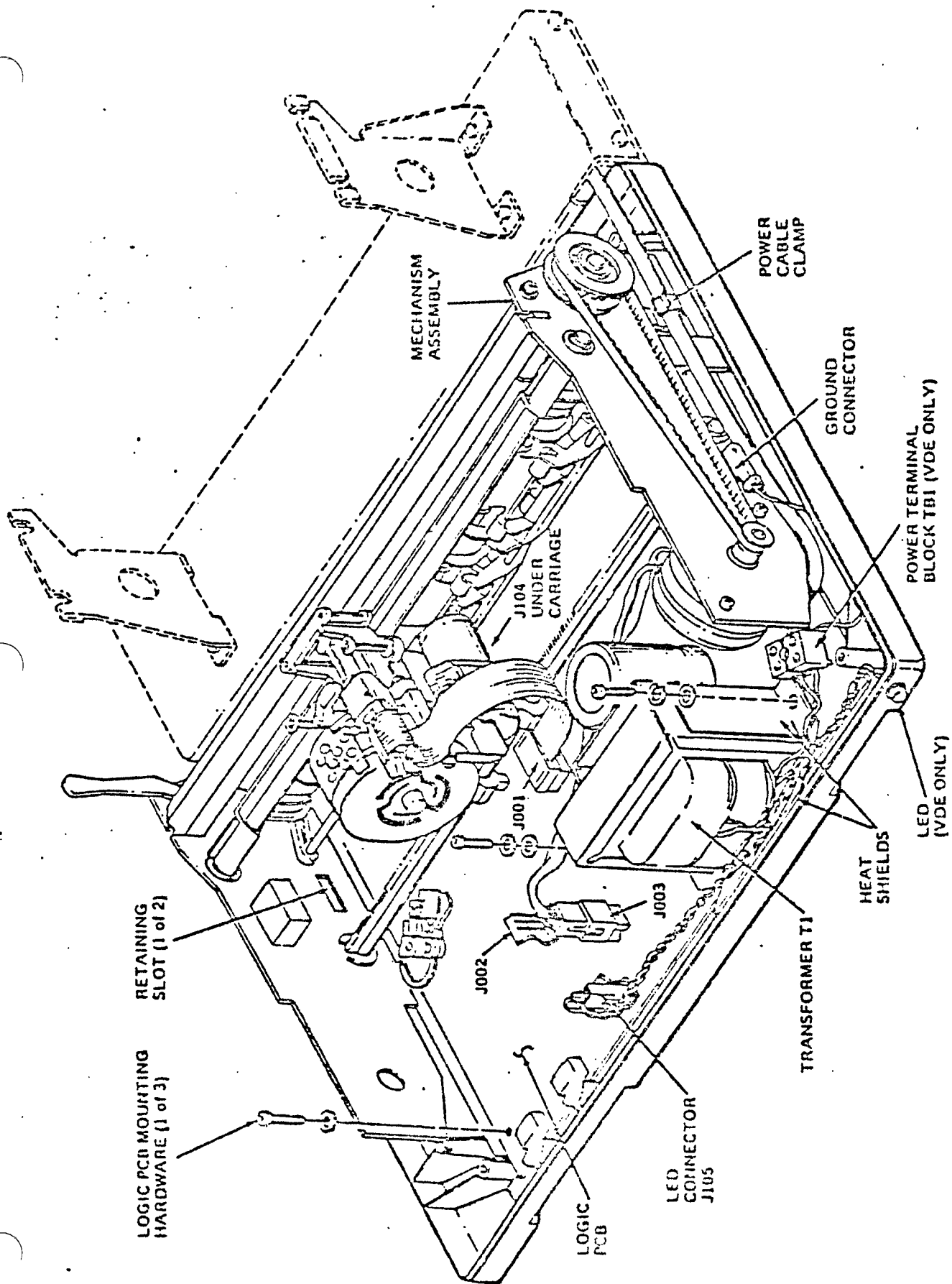


Figure 5-2a. LOGIC PCB REPLACEMENT

5.4 LINE FEED STEPPING MOTOR (Reference Figure 5-3)

To replace the line feed stepping motor assembly, removal of the assemblies is required as follows:

- a. Place power switch in the OFF position and remove plug from the wall outlet. Remove the cover assemblies.
- b. Remove the mechanism assembly. Be certain to read the CAUTIONS for removal/replacement of the mechanism assembly.
- c. Remove the two Phillips-head screws and mounting hardware and remove the line feed stepping motor assembly and drive belt.
- d. (If necessary) Remove the small pulley from the motor shaft using a 0.050" Allen hex key and install the pulley on the shaft of the replacement motor. Place it fully onto the shaft, but do not cause binding at the bearing. Tighten the Allen screw.
- e. Align the replacement motor at the mounting position and slip the belt onto the pulleys. Mount the motor using the hardware of step c.
- f. Remove the slack in the belt by drawing the motor toward the front of the printer, then tighten the hardware.

CAUTION

Do not over-tighten the belt, as this could result in premature failure of the stepper motor.

- g. Install the mechanism assembly and cover assemblies. Refer to steps b. and a. as required.

5.5 CARRIAGE RACK, MOLDED (Reference Figure 5-4)

To replace the carriage rack, removal of the assemblies and removal of the lower carriage shaft is required as follows:

- a. Place power switch in the OFF position and remove plug from wall outlet. Remove the cover assemblies.
- b. Remove the mechanism assembly.
- c. On the left side plate, remove the rack tension leaf spring mounting screw with a 6.5 mm (1/4") nut driver. Remove the retaining ring from the end of the lower carriage shaft and the retaining ring inside the left side plate.

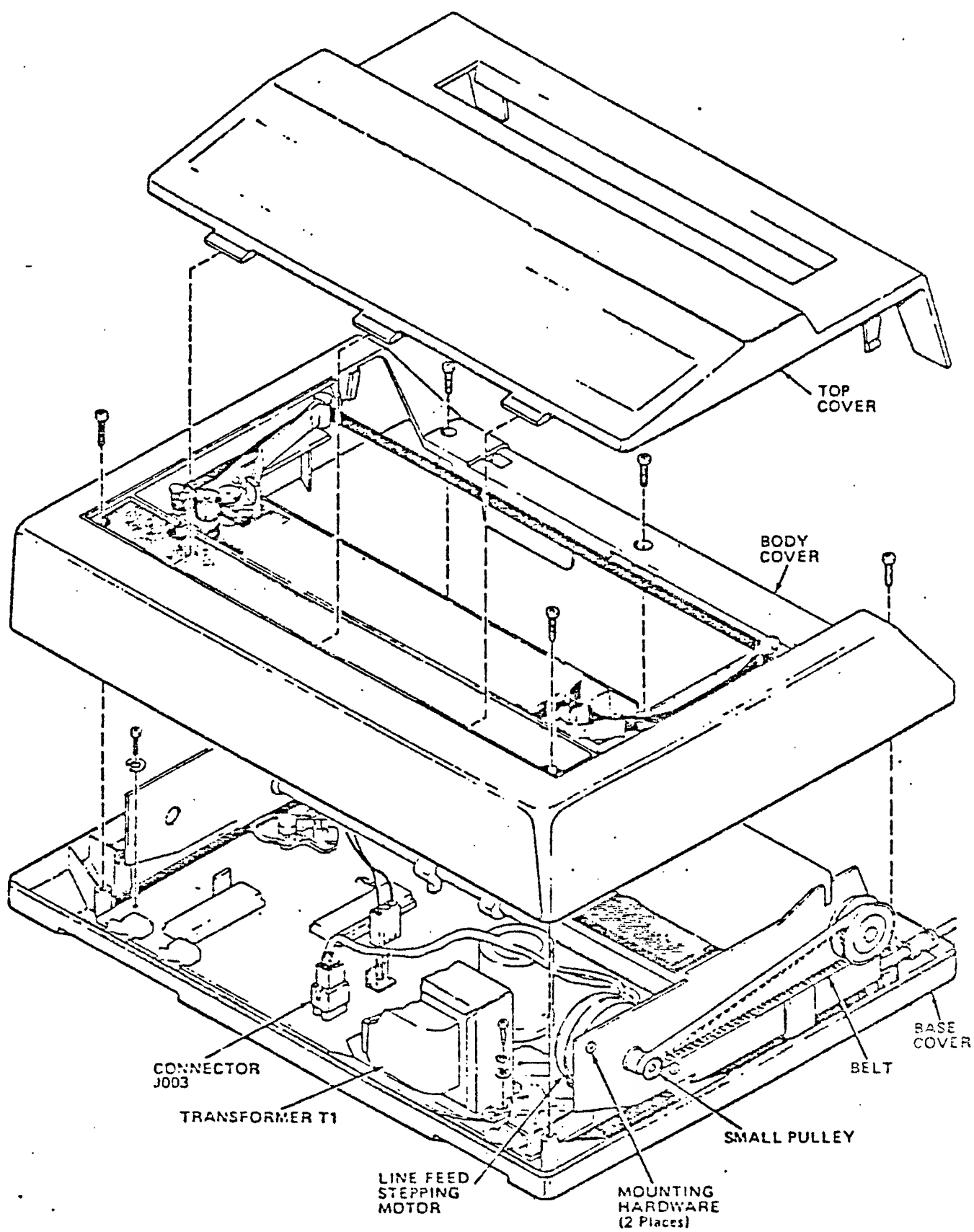
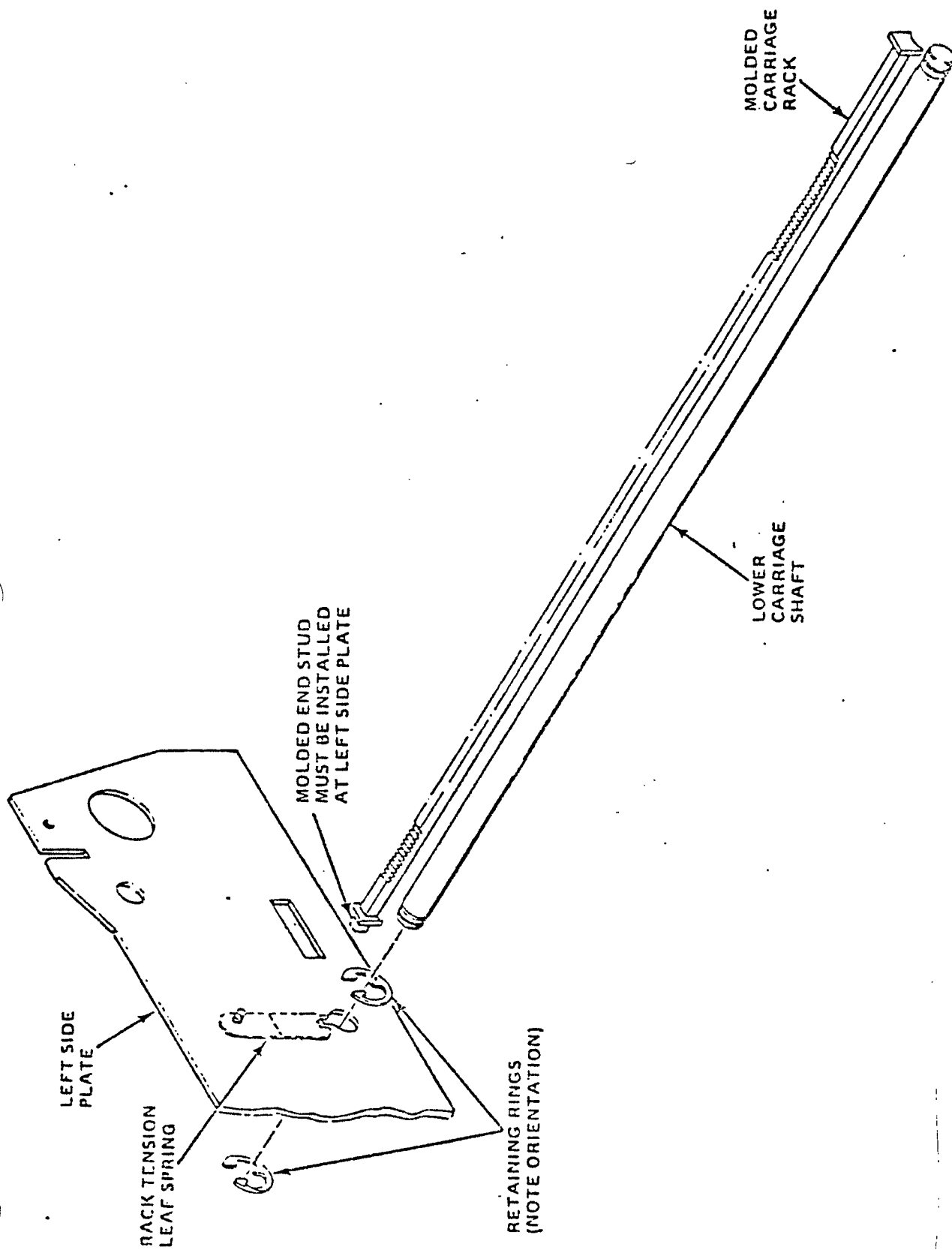


Figure 5-3. LINE FEED STEPPING MOTOR REPLACEMENT



- d. On the right side plate, remove only the inside retaining ring from the lower carriage shaft.
- e. Carefully slide the carriage shaft out of the right end plate. With the carriage shaft removed, the molded rack may now be removed.

CAUTION

The rack is not symmetrical, and must be installed so that the teeth are provided at the left end for the RTP function. The end with more teeth is also identified by a molded stud on the end.

- f. Insert the replacement rack, observing the CAUTION above. Insert the carriage shaft and orient the retaining ring on the right end so that it does not interfere with the end of the rack. Insert all retaining rings in this manner.
- g. Insert the rack tension leaf spring so that it tightens the rack.
- h. Install the mechanism assembly and cover assemblies, referring to steps b. and a. as required.

5.6 BASE COVER ASSEMBLY (Reference Figure 5-5).

To replace the base cover, remove the assemblies as follows:

- a. See the power removal WARNING (paragraph 5.1). Remove the cover assemblies.
- b. Remove the mechanism assembly...
- c. Remove the PCB logic board.
- d. Reinstall all assemblies on the replacement base cover. Refer to steps c., b. and a. as required.

5.7 BODY COVER ASSEMBLY (Reference Figure 5-6).

To replace the body cover assembly, proceed as follows:

- a. Place the power switch in the OFF position and remove plug from wall outlet. Remove the cover assemblies.
- b. Install the replacement body cover assembly consisting of the body cover, soundproofing, ribbon drive motor assembly, capstan and ribbon tensioner.
- c. Install a new Zip-Pak ribbon.

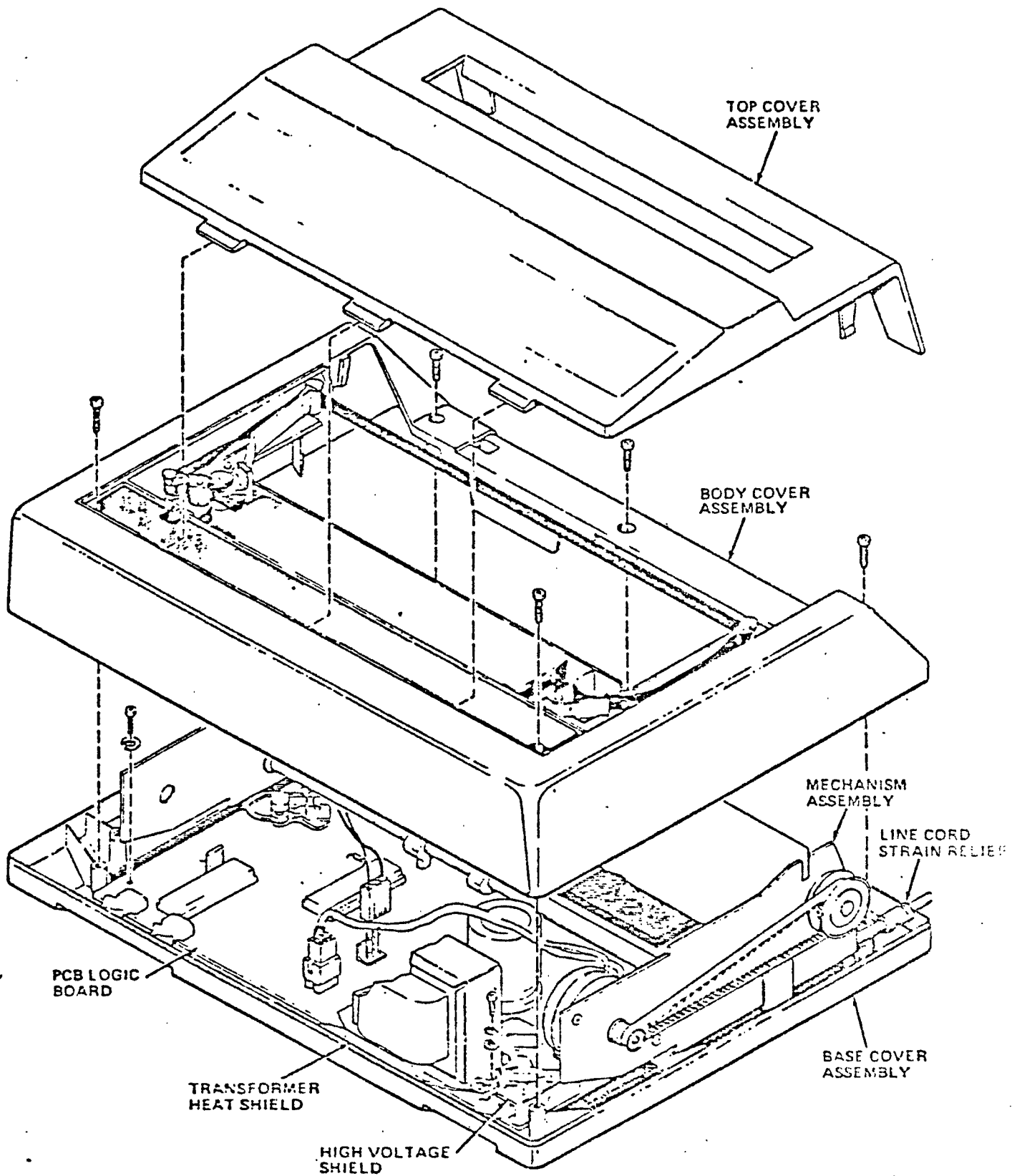


Figure 5-5. BASE COVER ASSEMBLY REPLACEMENT

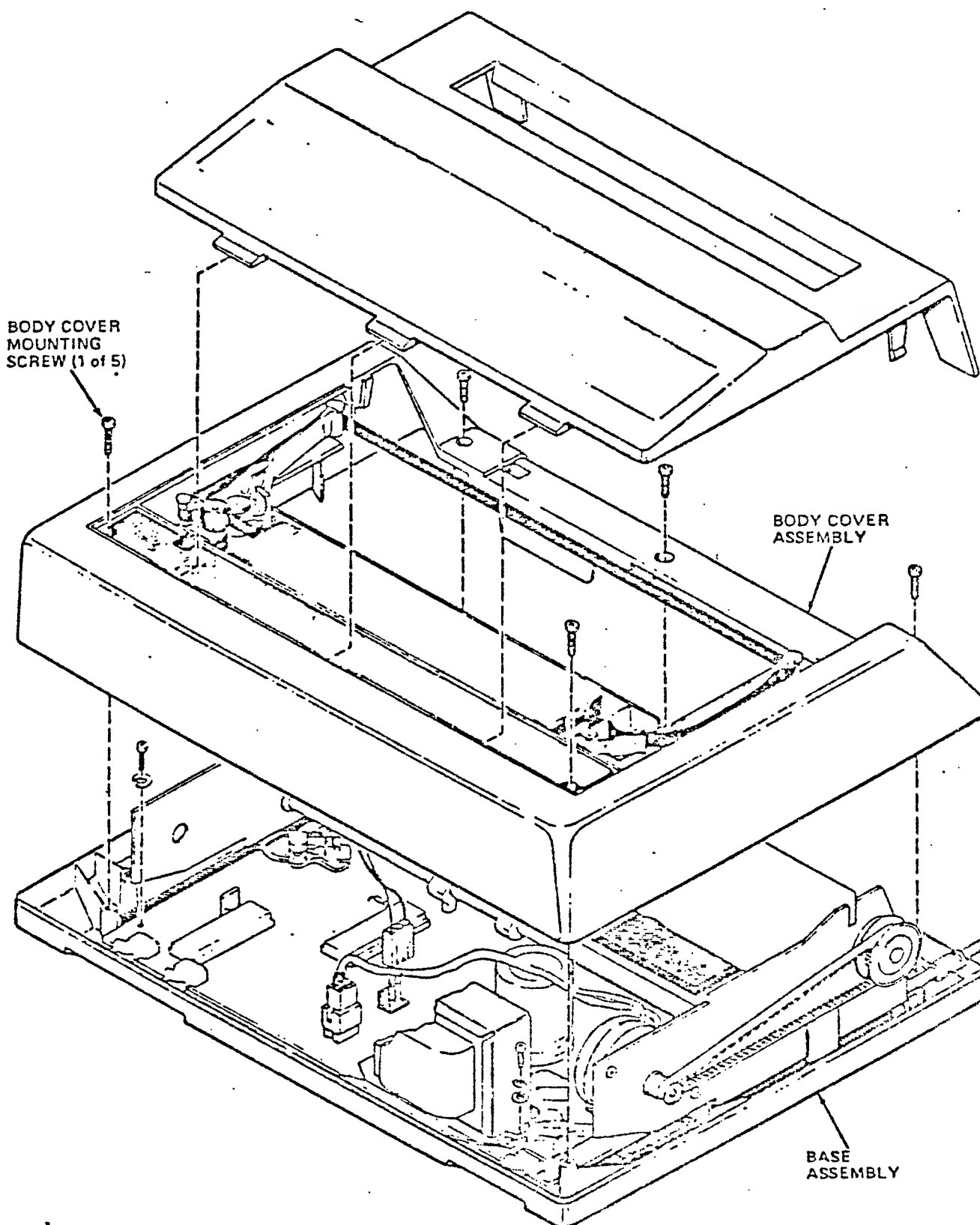


Figure 5-6. BODY COVER ASSEMBLY REPLACEMENT

5.8 HEAD-CARRIAGE ASSEMBLY (Reference Figure 5-7).

Replacement of the head-carriage assembly requires removal of the assemblies as follows:

- a. Place the power switch in the OFF position and remove plug from the wall outlet. Remove the cover assemblies.
- b. Remove the mechanism assembly.

CAUTION

When removing the carriage shafts, take care not to score the shafts, as this could result in rough carriage movement or excessive wear of the carriage. Also, the carriage must be supported and handled carefully during removal and replacement.

- c. Remove the two outside retaining rings from the carriage shafts at the right side plate, using a 1/4" flat blade screwdriver, then remove the four inside retaining rings from the carriage shafts.
- d. Remove the rack tension leaf spring from the left side plate using a 6.5 mm (1/4") nut driver.
- e. Carefully press the right ends of the carriage shafts through the side plate, and move them out through the left side plate.
- f. Remove the molded rack from the mechanism assembly and set the defective head-carriage assembly aside.
- g. Align the replacement head-carriage assembly and insert the nylon rack through the carriage bushings, then index the ends of the rack into the slots in the side plates. See CAUTION below.

CAUTION

The rack is not symmetrical, and must be installed so that the end with the molded stud is at the left side plate. The additional rack teeth are required for the RTP function.

- h. Install the carriage shafts and rack tension leaf spring, referring to steps e., d., and c. as required. See NOTE below.

NOTE

Before installing the mechanism assembly on the base, perform the clearance check/adjustment on the head-carriage assembly for the print-head to platen gap and the Hall effect sensor (Column Sense) and rotating magnet gap as detailed in Section 6.

- i. Install the mechanism assembly and cover assemblies, referring to steps b. and a. as required.

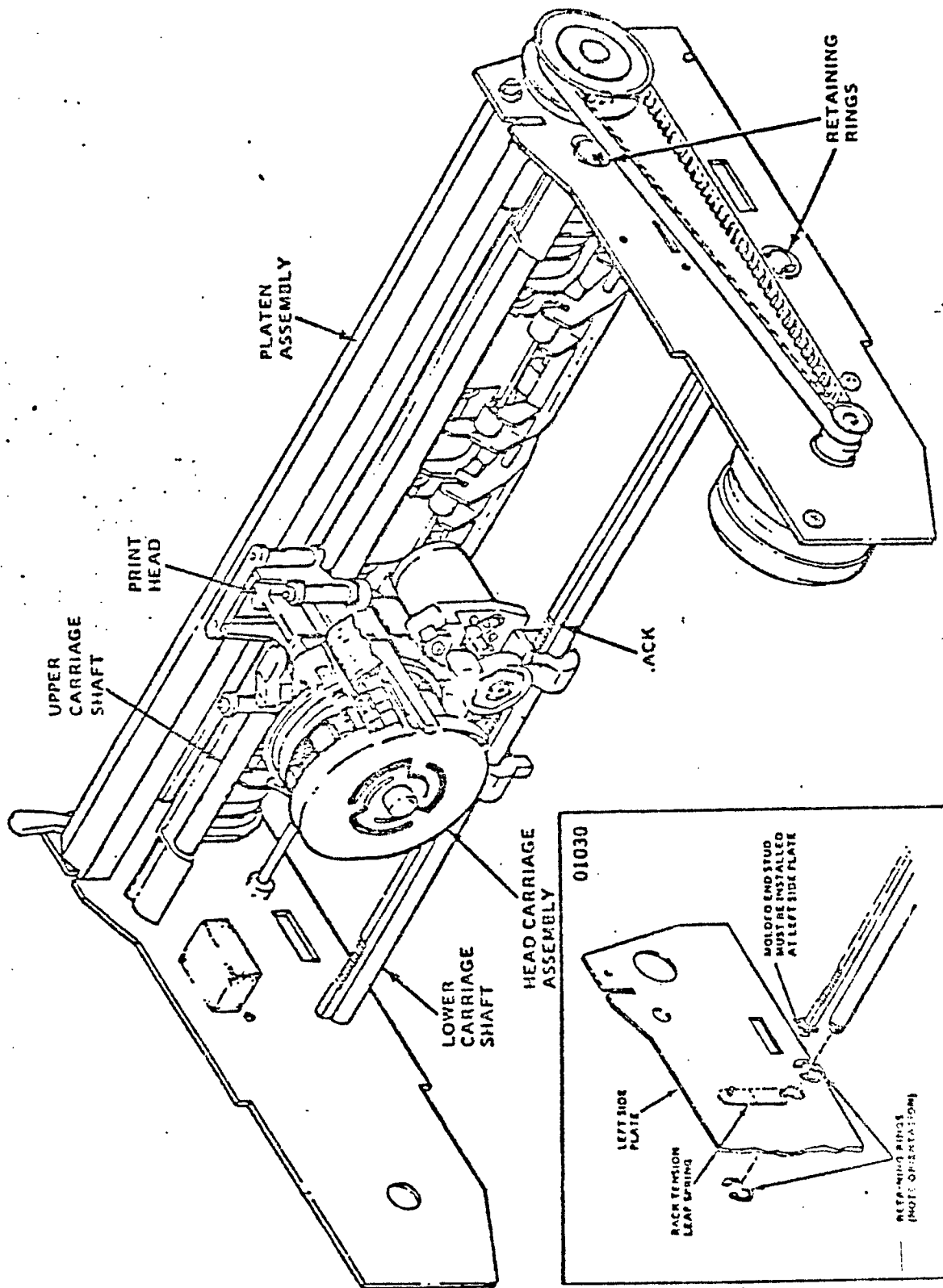


FIGURE 5-7. HEAD-CARRIAGE ASSEMBLY REPLACEMENT

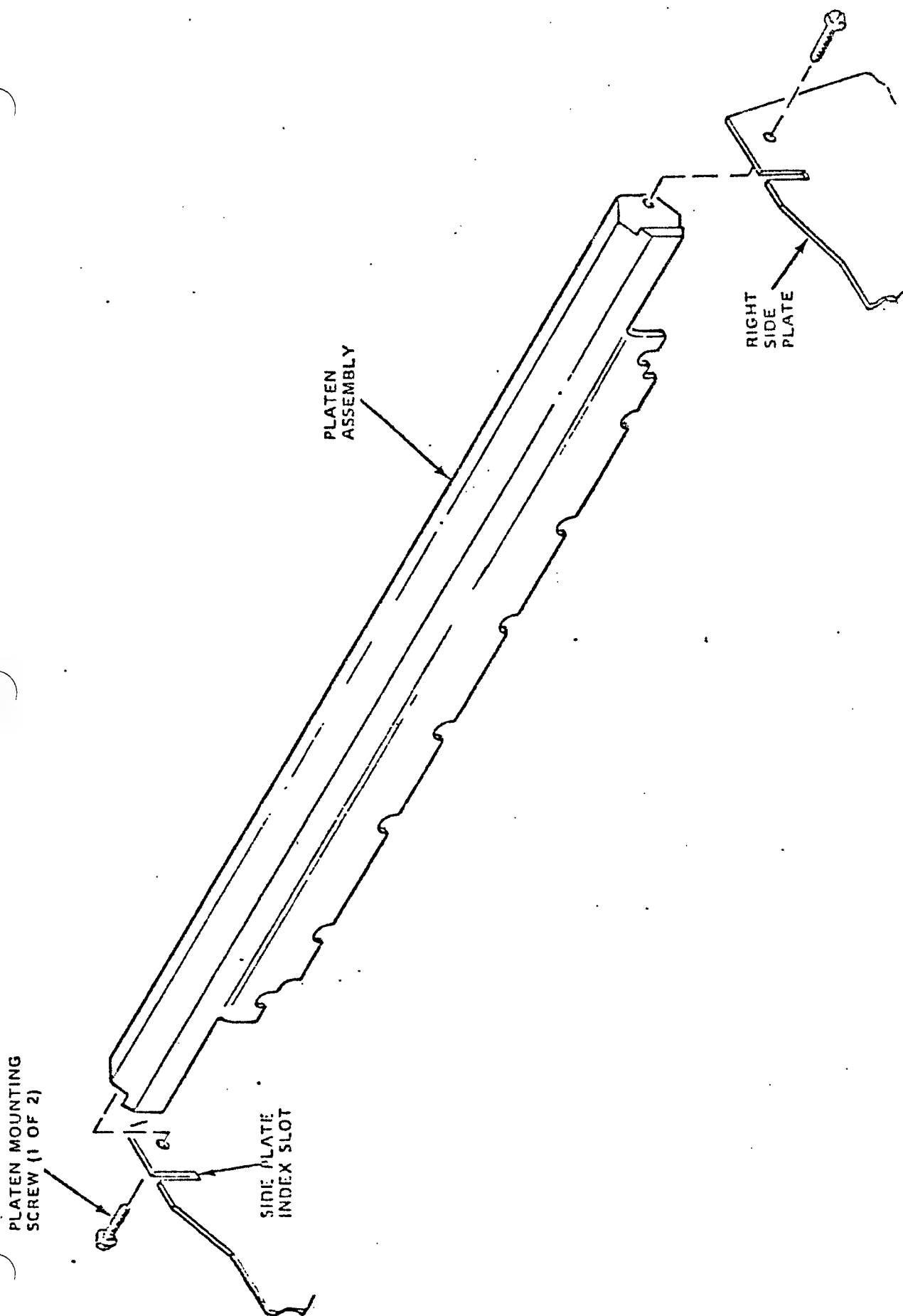


Figure 5-8. PLATEN ASSEMBLY REPLACEMENT

5.9 PLATEN ASSEMBLY (Reference Figure 5-8).

Replacement of the platen assembly requires removal of the assemblies as follows:

- a. Place the power plug in the OFF position and remove the plug from the wall outlet. Remove the cover assemblies.
- b. Remove the mechanism assembly.
- c. Remove one mounting screw from the side plate at each end of the platen using a 6.5 mm (1/4") nut driver.
- d. Remove the defective platen assembly by lifting upward at each end.
- e. Install the replacement platen assembly by inserting it firmly into the index slot at each side plate. Insert and tighten the 6.5 mm mounting screws.
- f. Check the print-head to platen gap and adjust if required .
- g. Install the mechanism assembly and cover assemblies, referring to steps b. and a. as required.

5.10 FUSE REPLACEMENT

Fuse F1 is located on the forward right-hand corner of the logic P.C. board. Refer to Figure 5-9 and proceed as follows:

1. Remove cover assemblies to gain access to logic P.C. board mounted on the base cover. (See procedure for removal of cover assemblies.)
2. Loosen screw located on right side of transformer using a Phillips screwdriver.
3. Remove the clear plastic high voltage shield covering the fuse and AC input circuits.
4. Remove and replace the defective fuse with a fuse of the same rating.
5. Reinstall the high voltage shield. Make sure that the slotted portion of the shield is under the flat washer.
6. Tighten the Phillips-head screw to secure the shield and transformer.
7. Reassemble the cover assemblies.

5.11 DISABLING OF AUTO LINE FEED ON CARRIAGE RETURN

The auto line feed on carriage return function may be disabled by removing resistor R11 on the logic P.C. board. To disable this function, refer to Figure 5-9 and proceed as follows:

1. Remove cover assemblies to gain access to logic P.C. board mounted on base cover. (See procedure for removal of cover assemblies.)
2. Locate resistor R11 and cut the lead that is most distant from the large IC chip, using side cutters.
3. Bend resistor upward away from P.C. board and cut second resistor lead.
4. Reassemble the cover assemblies.

The auto line feed on carriage return function may be re-enabled by installing a new resistor (R11, 270 ohms, 1/4 watt). Use a 25 to 35 watt soldering iron and wicking braid or a solder sucker to remove old solder. Then, solder the new resistor in place. Before installing the resistor, trim and form leads so they do not extend more than 1/8 inch through the printed circuit board.

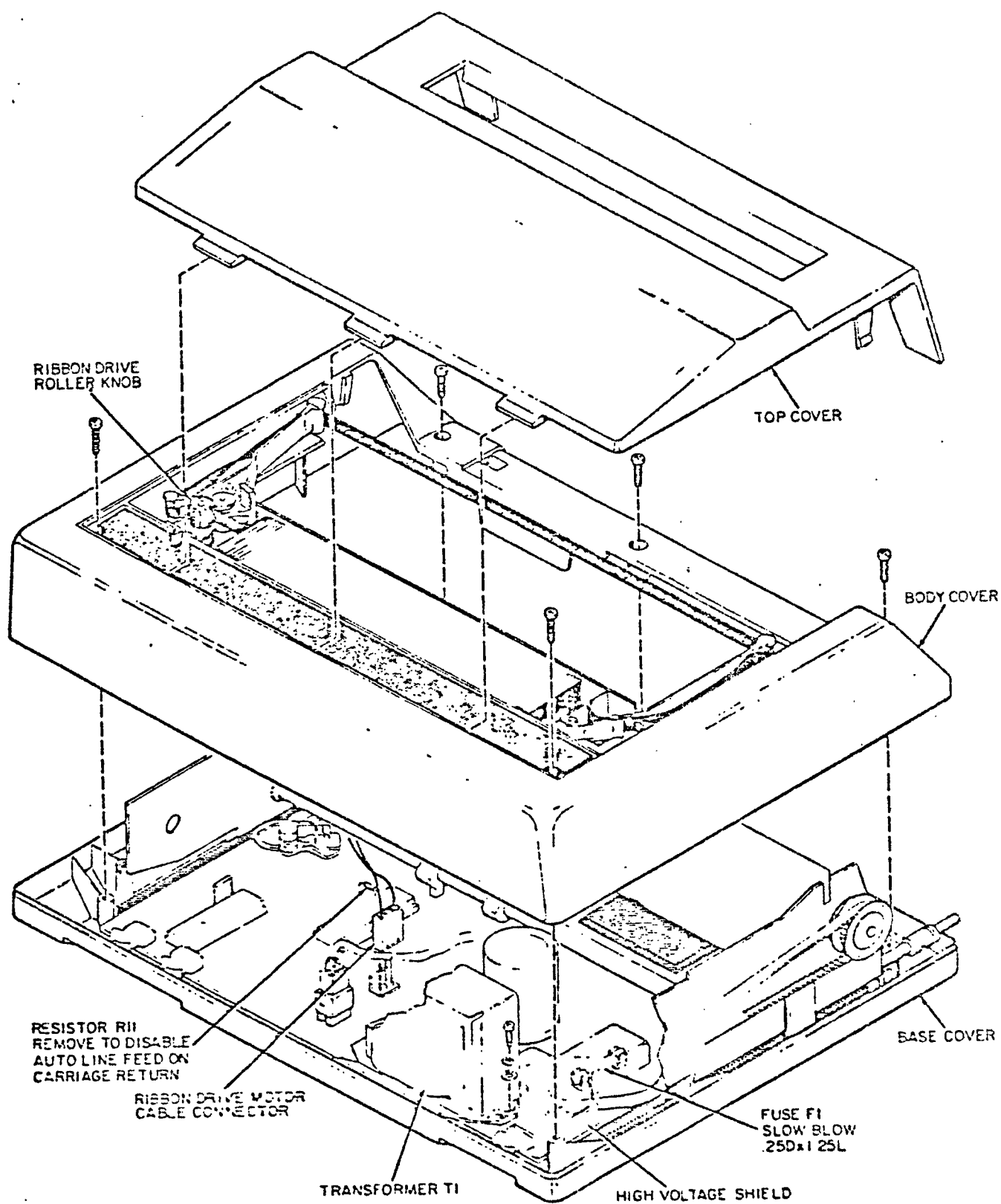


Figure 5-9. REPLACEMENT OF FUSE F1 AND AUTO LINE FEED DISABLE

SECTION 6
SUPPORTING ILLUSTRATIONS

6.1 PURPOSE OF THIS SECTION

This section contains drawings and schematics to aid in maintaining and troubleshooting the ATARI 825 Printer.

6.2 MAIN LOGIC BOARD SCHEMATIC (63680147-4001)

The schematic for the Main Logic Pcb is contained in Figure 6-1.
THIS IS THE EARLIER PRINTER.

6.3 FLYBACK BOARD SCHEMATIC (6368172-4001)

The schematic for the Flyback Pcb is contained in Figure 6-2.

6.4 MAIN LOGIC BOARD SCHEMATIC (63680193)

The schematic for the Main Logic Pcb is contained in Figures 6-3a, b, c.
THIS IS THE UPDATED PRINTER.

6.5 MAIN LOGIC SILKSCREEN

This silkscreen is contained on Figure 6-4. THIS FOR THE UPDATED VERSION.

6.6 INTERCONNECTION SCHEME

Figure 6-5 shows the interconnection of the various electrical and electronic components of the ATARI 825 Printer.

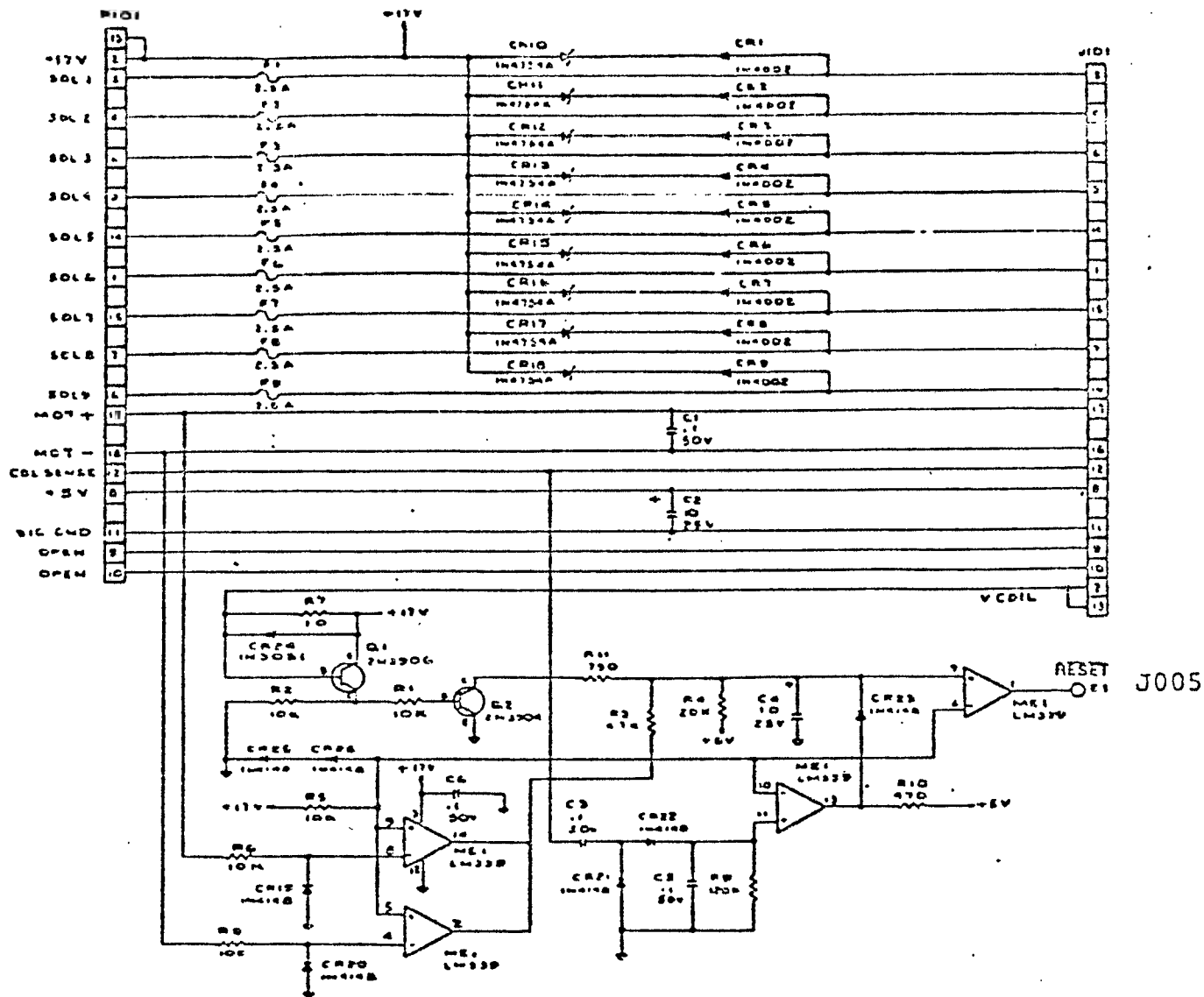
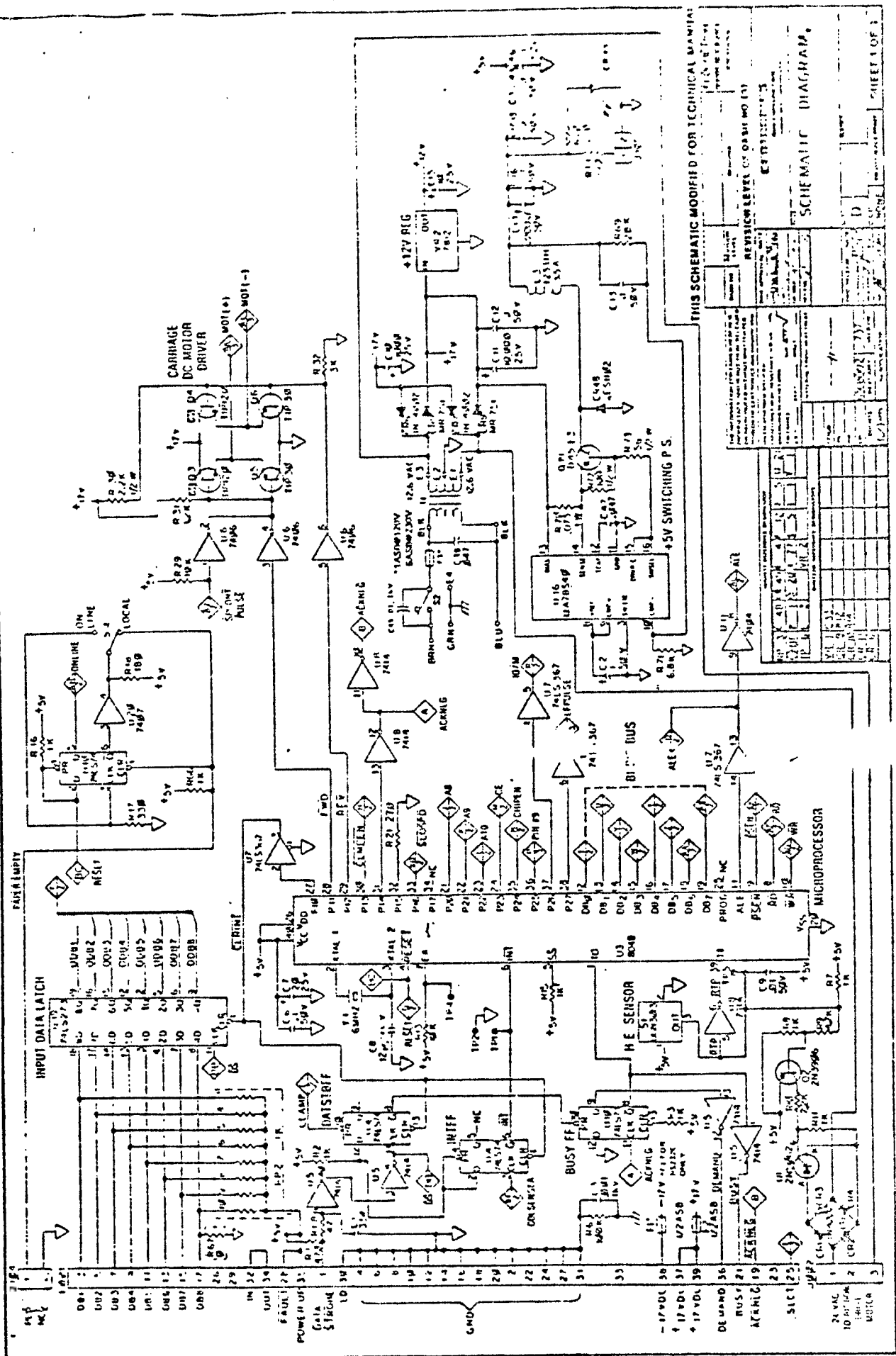


Figure 6-2 FLYBACK BOARD



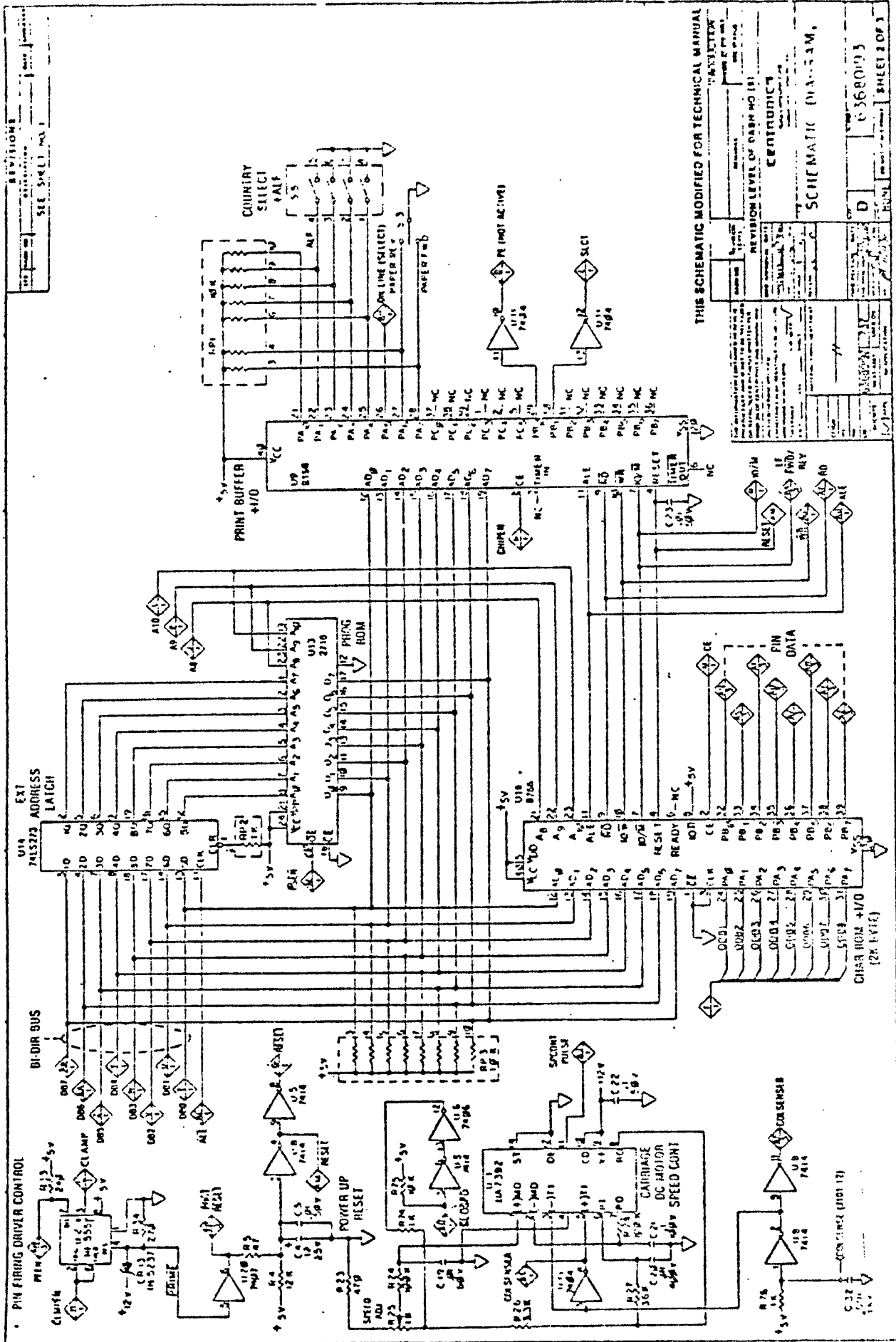


Figure 6-3b MAIN LOGIC SCHEMATIC (SH2)

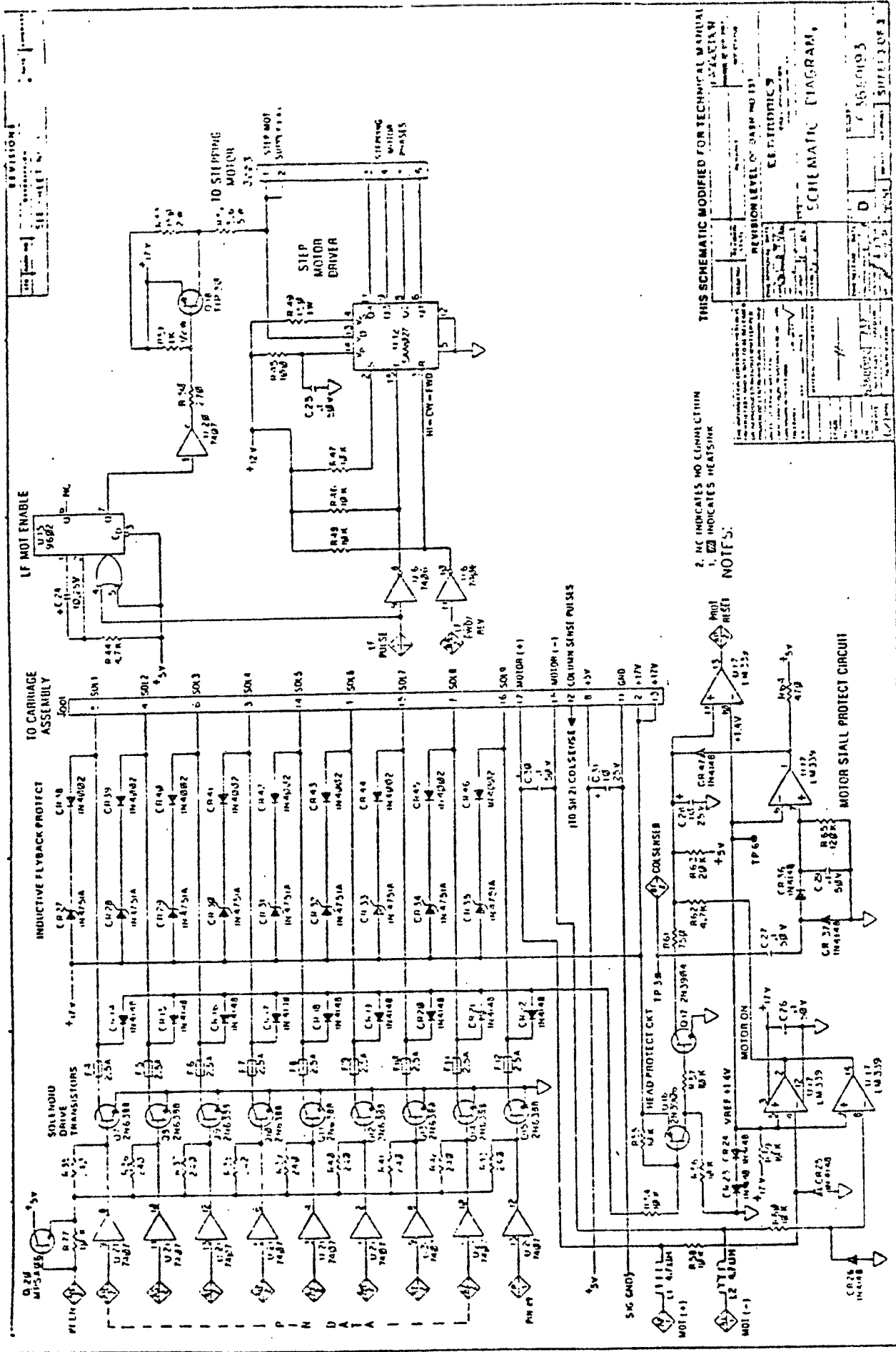


Figure 6-3c. M/TN LOGIC SCHEMATIC (SH3)

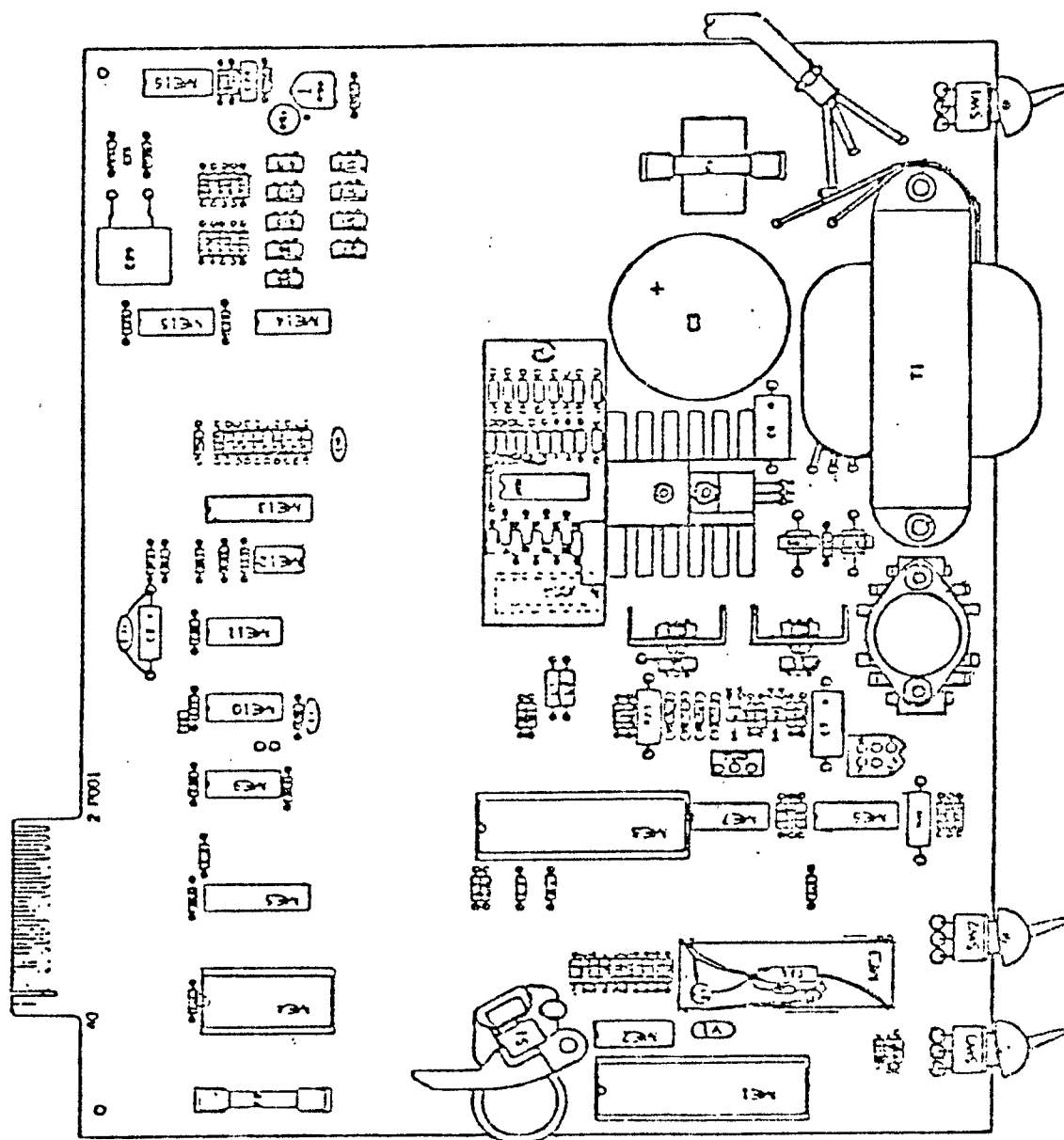
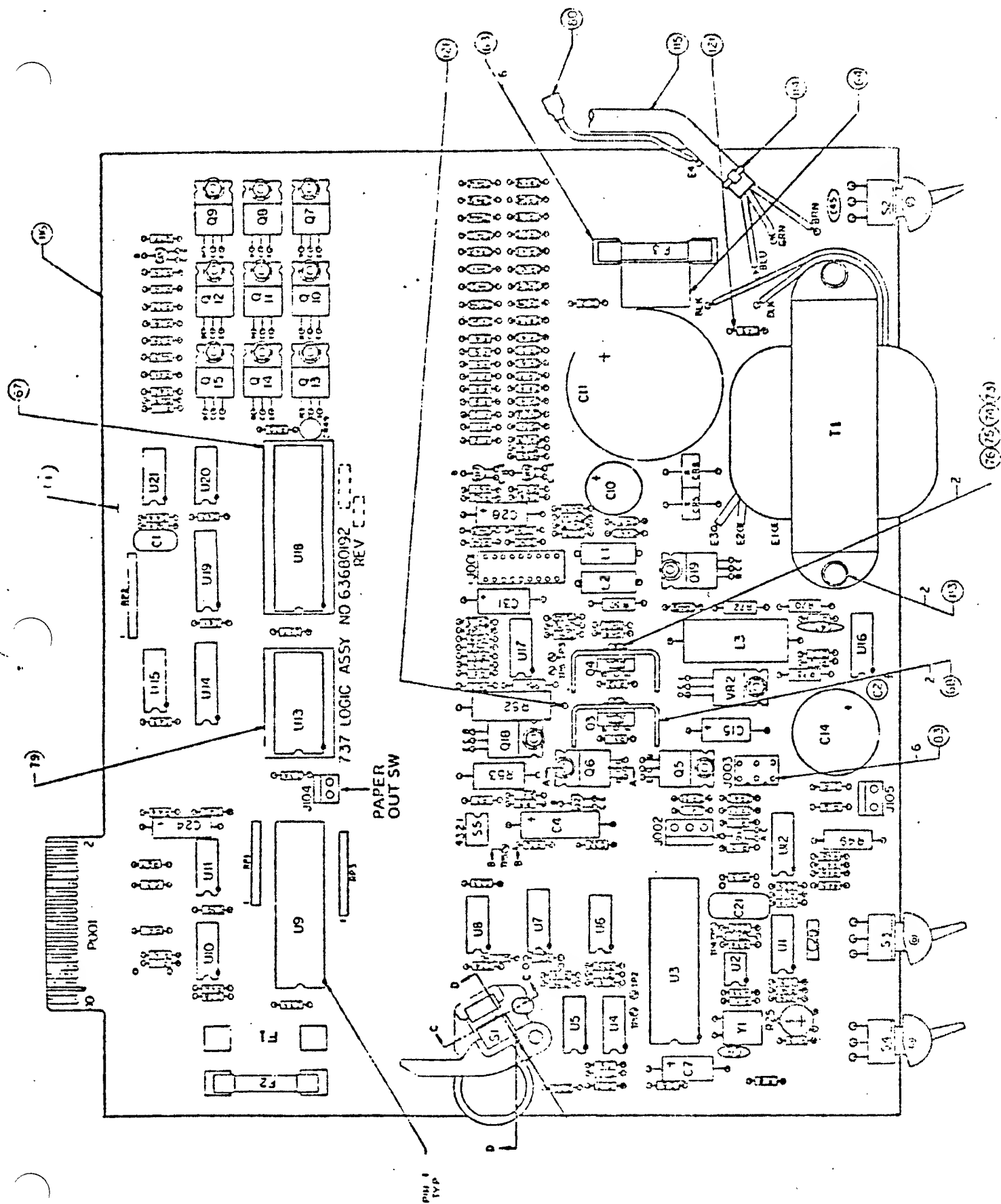


Figure 6-4 LOGIC PCB ASSEMBLY



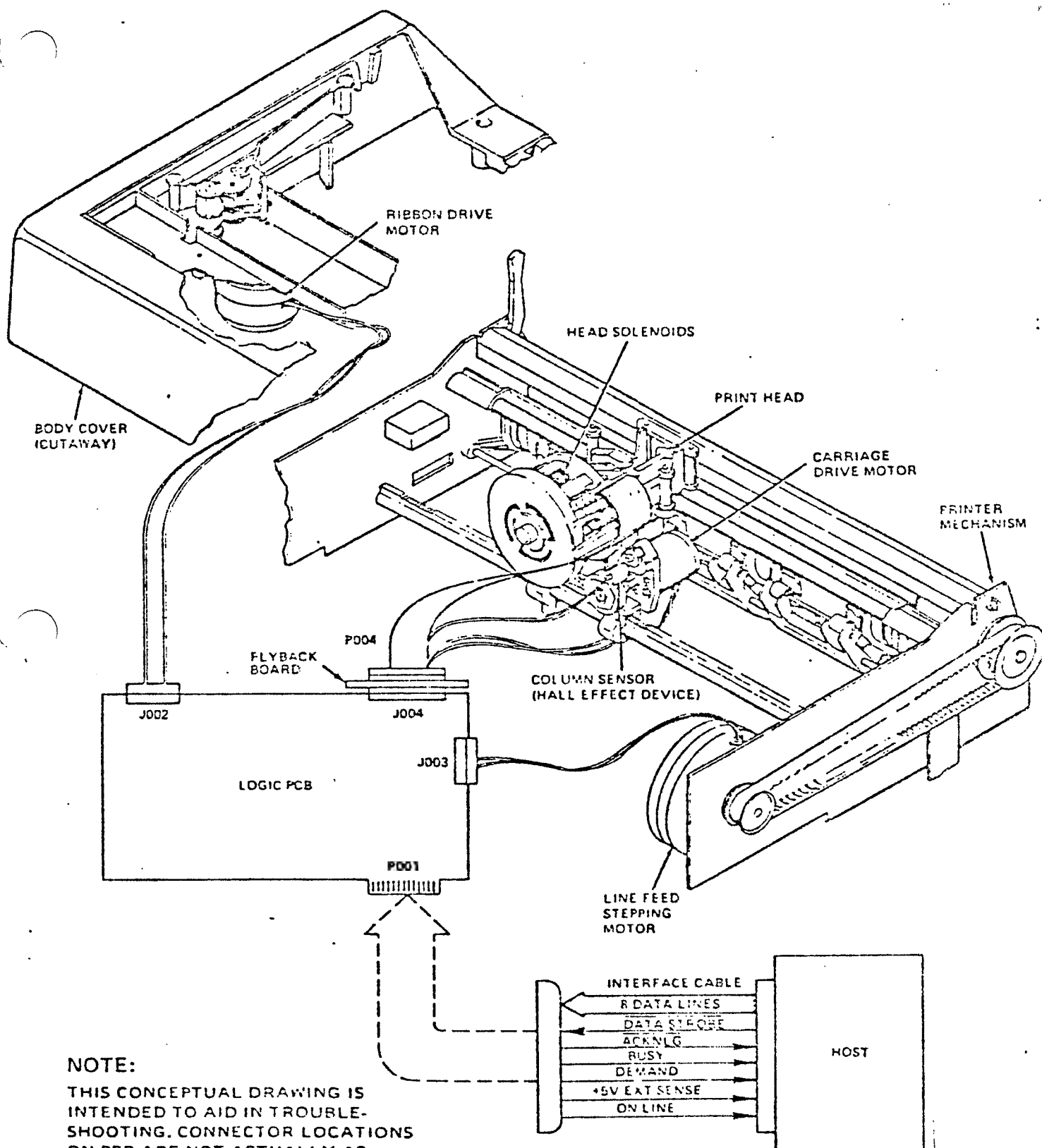


Figure 6-5.

PRINTER INTERCONNECTION SCHEME

SECTION 7

7.1 PARTS LIST

Table 7-1 is a list of recommended parts for the ATARI 825 printer. Included for each item are the recommended quantities based upon machine population. This list of parts is recommended for service locations where the major subassemblies are replaced.

Table 7-1. PARTS LIST

Description	Centronics Part Number	CPS Part No.	Quantity required per Machine Population		
			100-250	251-500	501-1000
Mechanism Assy	63680202-5003	FA100480	12	22	40
Logic Pcb (old)	63680147-4001	FA100481	10	20	30
Logic Pcb (updated)	63680193	FA100482	8	12	20
Ribbon Drive Motor Assy	63669271-4001	FA100483	2	3	6
Switch, OFF/ON, LOCAL	39090010-1001	FC100484	1	2	4
Head/Carriage Assy	63669278-5002	FA100485	0	1	2
Print Head Assy	62001100-5006	FA100486	1	2	4
Print Head Assy (updated)	62001211-5002	FA100487	1	2	4
Flex Ribbon Cable (9 wire)	63680142-4001	FA100488	2	3	6
Fluback Pcb	63680172-4001	FA100489	2	3	6
Printer Drive Assy	63669264-4001	FA100490	2	3	6
Upper Motor Assy	63680114-4001	FA100500	0	1	2
Carriage Rack, Molded	63669240-2001	FC100501	1	2	4
Fuse, Picofuse 2.5A	39030030-1005	FC100502	15	25	45
Fuse, Slow Blow 1A	39030000-1001	FC100503	10	20	40
IC MPU 8049	63669503-4004	FC100504	2	3	6
IC EPROM 8755	35578755-1A52	FC100505	2	3	6
IC RAM 8156	35578156-1001	FC100506	2	4	6
IC Driver 7407	35474070-1900	FC100507	1	2	4
Head Adjust Cam	63669262-2001	FC100508	5	10	20
Solenoid Driver 2N6388	38263880-1900	FC100509	2	4	6

Atari Part Number

IC Timer 555	C019748	2	3	7
I/O Cable Assy	CA015544	4	8	15
I/O Cable Assy (CX-86) Pkgd	CA015900-02			

REPLACEMENT PART SOURCES

<u>Description</u>	<u>CPS P/N</u>	<u>Other Vendor P/N</u>
Fuse, Picofuse 2.5A	FC100502	270512 Mfg LITTLEFUSE Vendor ZACK Electronics
Fuse, Slow Blow 1A	FC100503	313001 Mfg LITTLEFUSE Vendor ZACK Electronics 27F692 Mfg LITTLEFUSE Vendor NEWARK Electronics
Solenoid Drivers (2N6388)	FC100509	2N6388 Mfg RCA Vendor NEWARK Electronics
Driver (7407)	FC100507	SN7407N Mfg TI Vendor NEWARK Electronics

SECTION 8

SIGNAL WAVEFORMS

8.1 PURPOSE OF THIS SECTION

This section contains signal waveforms descriptions. The waveforms are contained on Figure 8-1.

8.2 Set 1 waveforms.

Set 1 waveforms are present with or without a host computer. They are always present and must be correct as they are the system prime clocks and control signals.

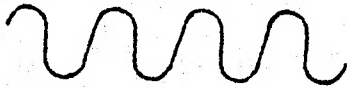
8.3 Set 2 waveforms require the T825 Test Program to stimulate the printer electronics. Must run full function program to complete speed adjustment.

NOTE: For all signals, PW is defined as pulse width, with the waveform measured from leading to trailing edges. PD is defined as pulse duration with the waveform measured from leading edge to leading edge.

SET 1 WAVEFORMS

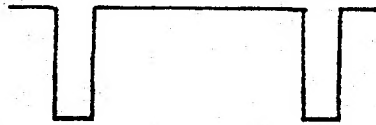
ME3 Pin 2
U3 pin 2
(XTAL1)

Master clock 6Mhz



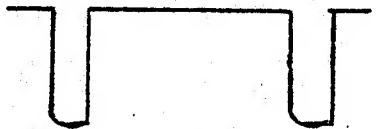
PW 1.7us (1.66us calculated)

ME3 Pin 38
U3 pin 38



PW 5us PD 11-12.5ms

ME3 Pin 11
U3 pin 11
(ALE)



PW .6us PD 2.5us

ME3 Pin 9
U3 pin 9
(PSEN)



PW .55us PD 2.5us

ME3 Pin 8
U3 pin 8
($\overline{AD}$)



PW 1.2us PD 130us

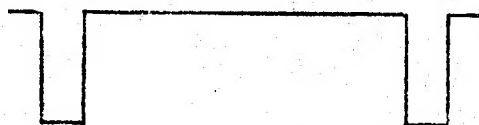
SET 2 WAVEFORMS

ME8 Pin 7
U8 pin 7
(IO/M)



PW 80us
PD 1ms

ME3 Pin 6
U3 pin 6 Speed adjust (R59)
(INT)



PD 2.0-2.1ms

Figure 8-1.

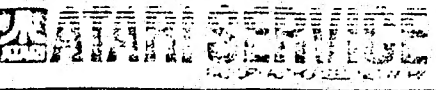
SECTION 9

GLOSSARY OF TERMS

NOTE: For expanded discussion of terms and details reference Theory of Operation.

- 1) +5V Reg - power for all logic
- 2) +5V EXT - power for host device sense
- 3) +12V Reg - power for motor drive
- 4) +17V Unreg - power for solenoid and motor drive
- 5) 24VAC - power for ribbon motor
- 6) Basic clock - 6MHz, provides the time base for all timing pulses, motor steps and sequences
- 7) CR - carriage return
- 8) LF - line feed
- 9) RESET - starts the power-up and initialization routines
- 10) RTP - ready to print, true when carriage is positioned in column one, far left
- 11) EA - external access
- 12) COLSENSE - column sense; for carriage sensor; generates interrupt to processor; 100 cosenses per inch, 800 per line.
- 13) DB0-DB7 - bi-directional bus for transferring data, address & column patterns for pin driving
- 14) ALE - address latch enable; suit to 256 bit character buffer I/O, I.C. character ROM & address latch buffer.
- 15) PSEN - Program store enable; sent to Program (firmware) ROM for output enable placing contents of location addressed on the bus.
- 16) OE - ROM output enable
- 17) CLMPEN - Clamp enable triggers current source for pin drivers.
- 18) CLRINT - Clear interrupts resets bi-directional bus latch, data stroke flip-flop and busy flip-flop
- 19) FWD & REV - Forward and reverse control the direction of the carriage drive motor

- 20) PAP FWD/REV - Paper forward/reverse controls direction of the line feed stepping motor; low allows normal (forward) line feed
- 21) MTRSTP - Motor step pulse generated by internal timer in microprocessor.
- 22) INT - Interrupt to microprocessor generated by COLSENSE flip-flop
- 23) IO/M - Input output/memory function; same line as PAP FWD/REV; low selects RAM in character buffer I.C. and ROM in ROM I/O I.C.
- 24) CE - Chip enable
- 25) RD - Read the bus; stroke to enable data onto the bus from character ROM I/O I.C. and character buffer I/O I.C. for microprocessor to read. Also allows microprocessor to print character code from RAM or read switch settings on Port A (ODB1-ODB8).
- 26) WR - Write, an output stroke used to write data into an outboard memory used by character buffer I.C. to write data into RAM or Port B; used by character ROM to write pin driving pulses onto Port B.
- 27) Data Stroke - Data stroke used to latch data into Input Data Latch
- 28) PRIME - used to preset Data Stroke FF & Busy FF to inhibit host data transmission and shut off bias to the solenoid drive transistors.
- 29) ACKNLG - used to acknowledge commands from host computer.



FIELD DISCREPANCY NOTICE

(FOR ATARI USE ONLY)

PLEASE PRINT CLEARLY

NAME	MODEL NUMBER
LOCATION/ADDRESS	PART NUMBER/REVISION LEVEL
	PART DESCRIPTION
	MANUFACTURING DATE CODE
TELEPHONE NUMBER	QUANTITY

WHERE DETECTED:

☐ INDEPENDENT SERVICE CENTER	OTHER (SPECIFY) _____
☐ REGIONAL SERVICE CENTER	_____

SAVE FAILED PARTS FOR 15 WORKING DAYS BEFORE DISCARDING:

DESCRIPTION OF DISCREPANCY/OBSERVATION

ORIGINATOR	REGION	DATE
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